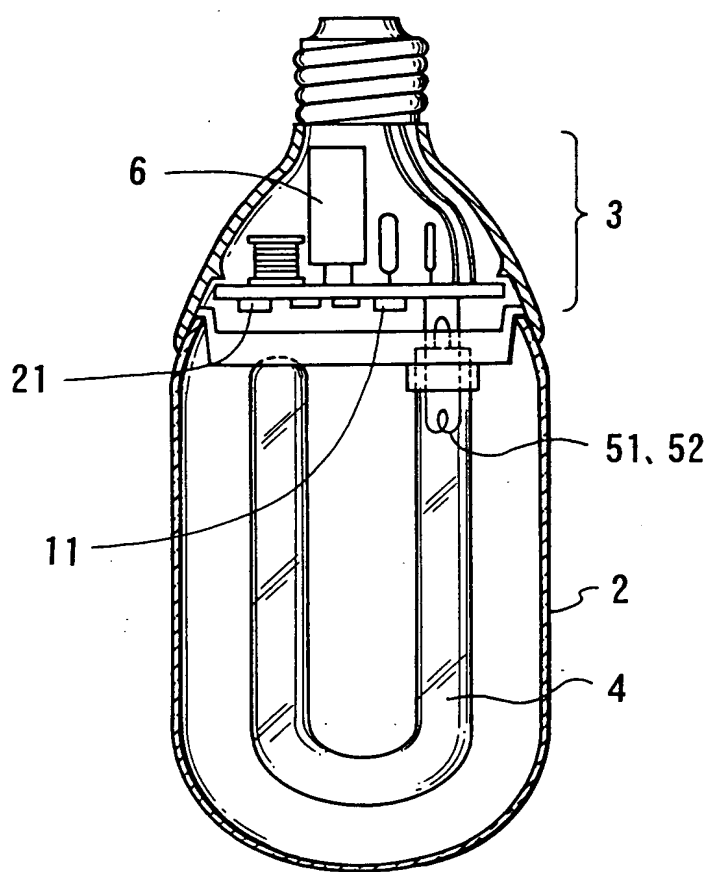
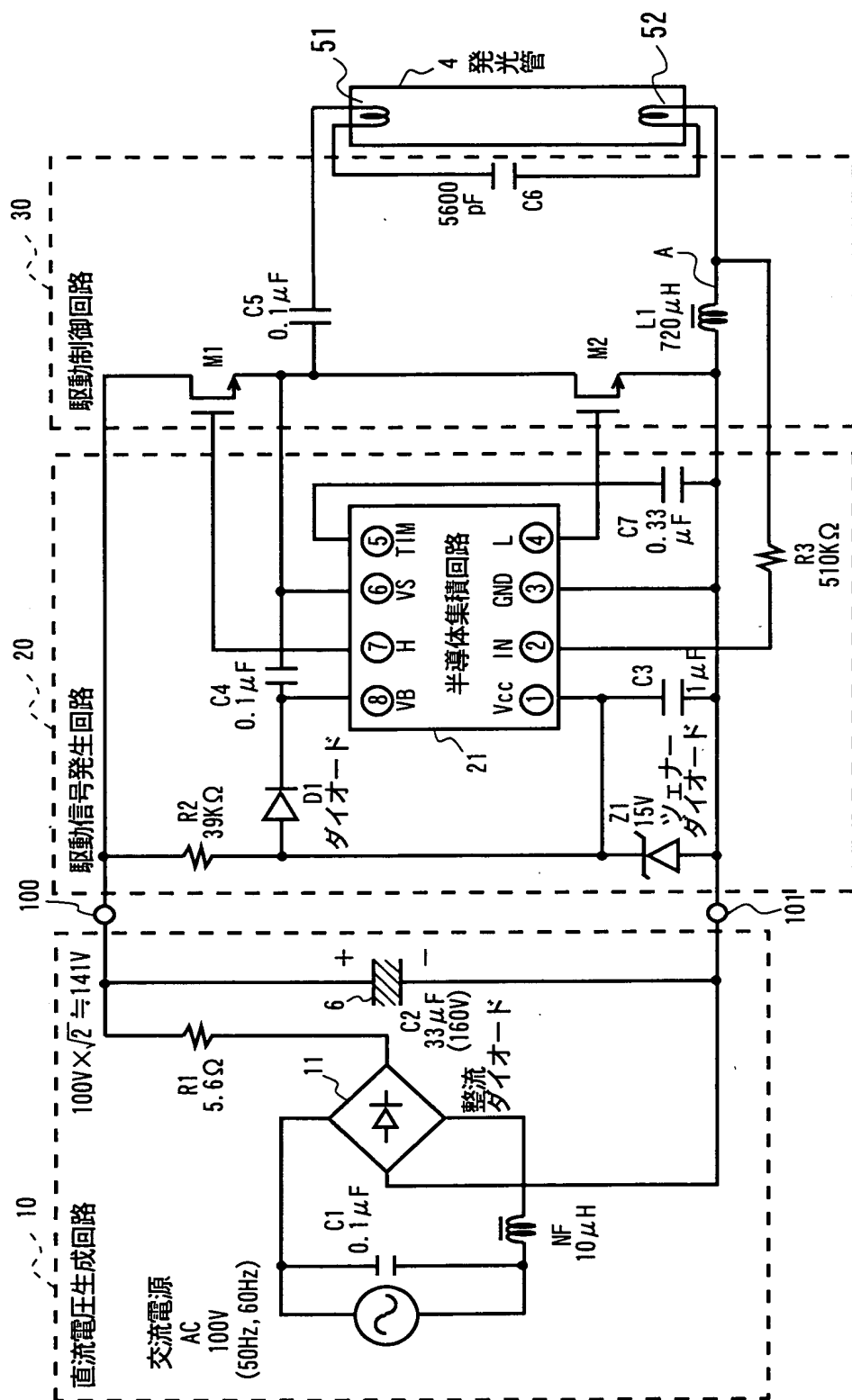


图 1





2
✕

图 3

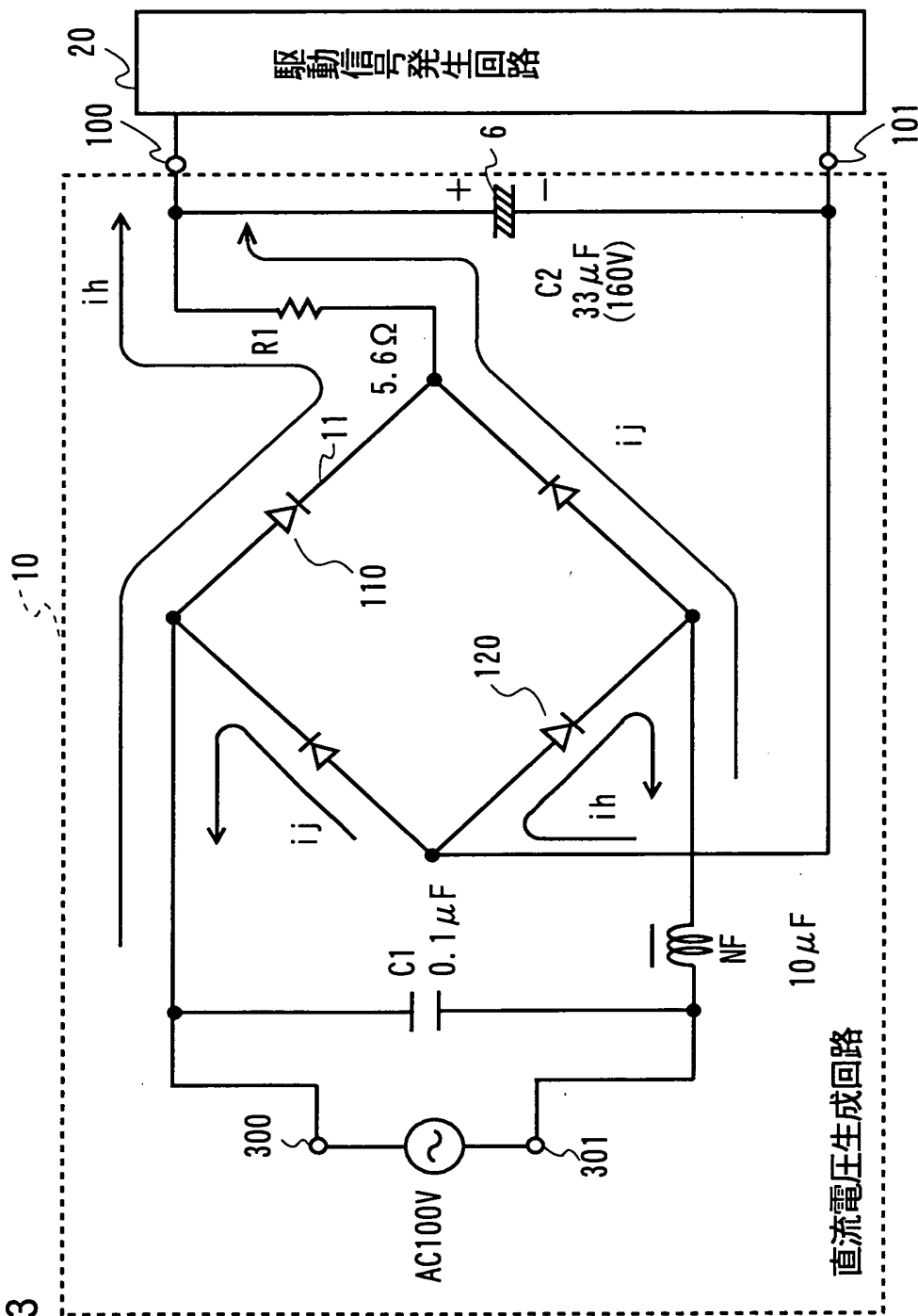


図4

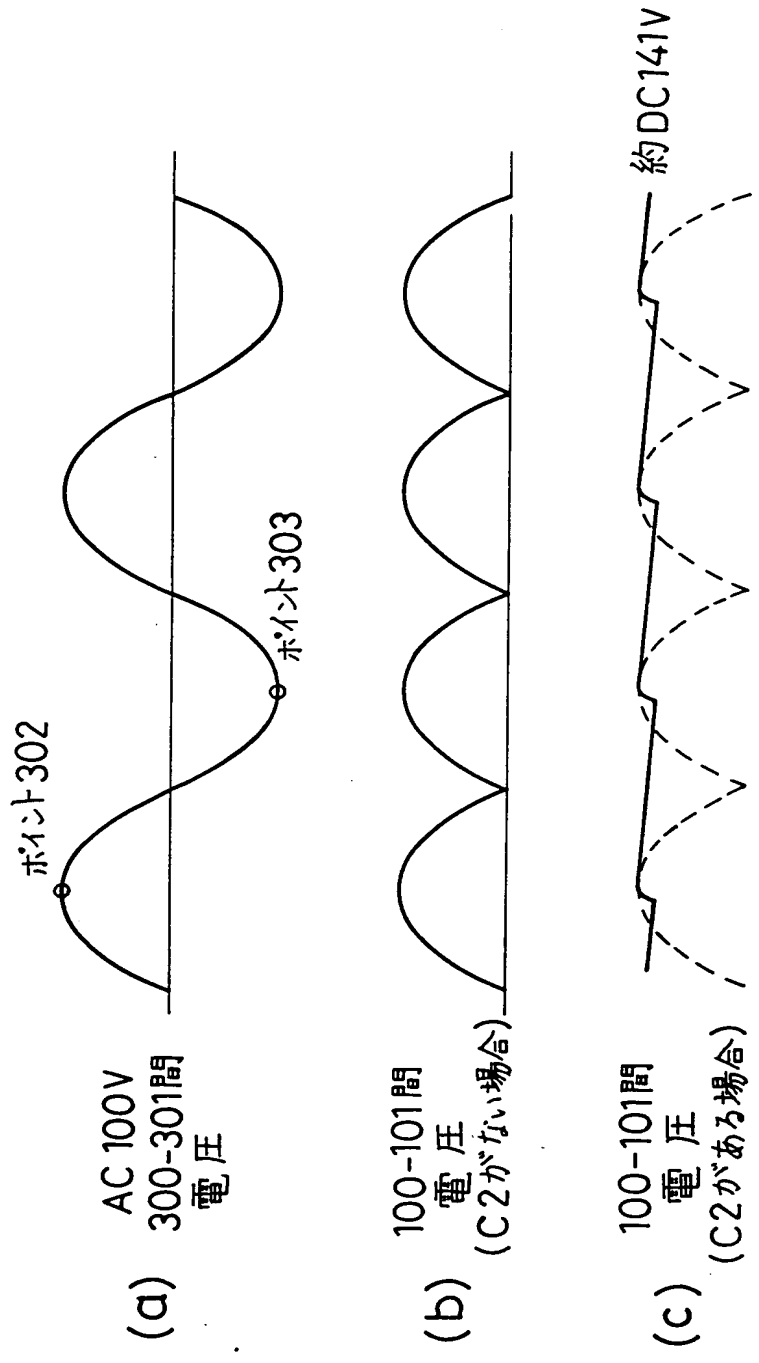


図5

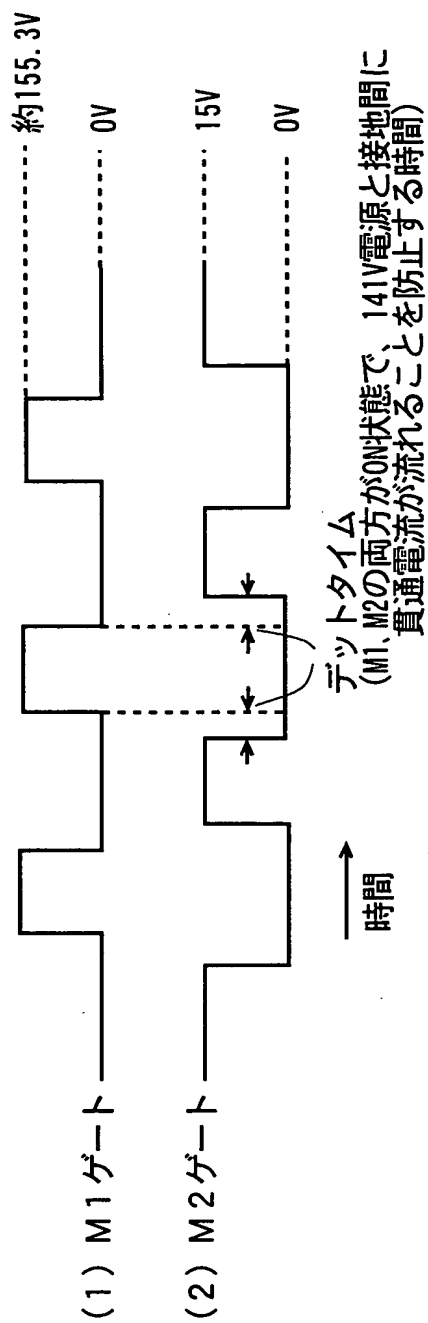


図6

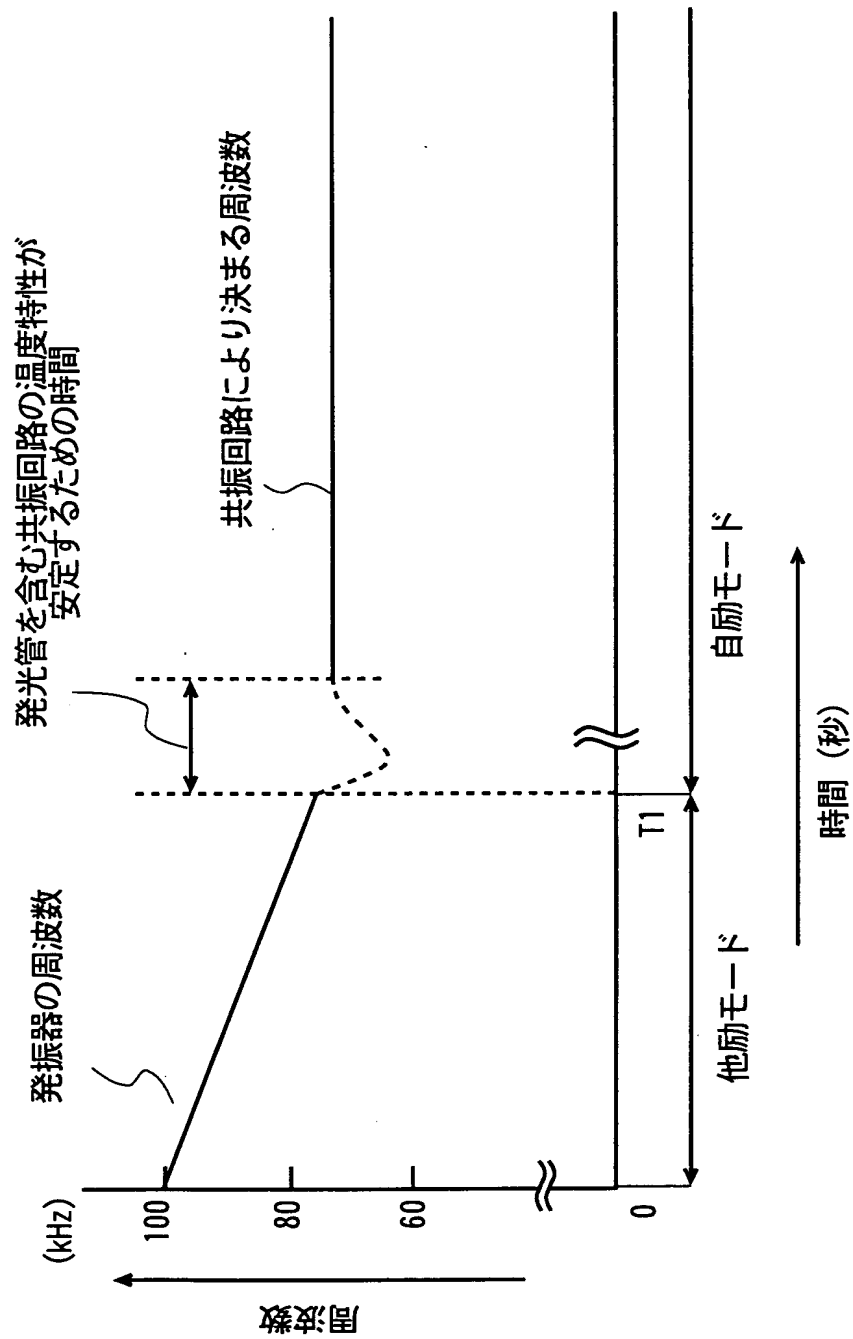


図7

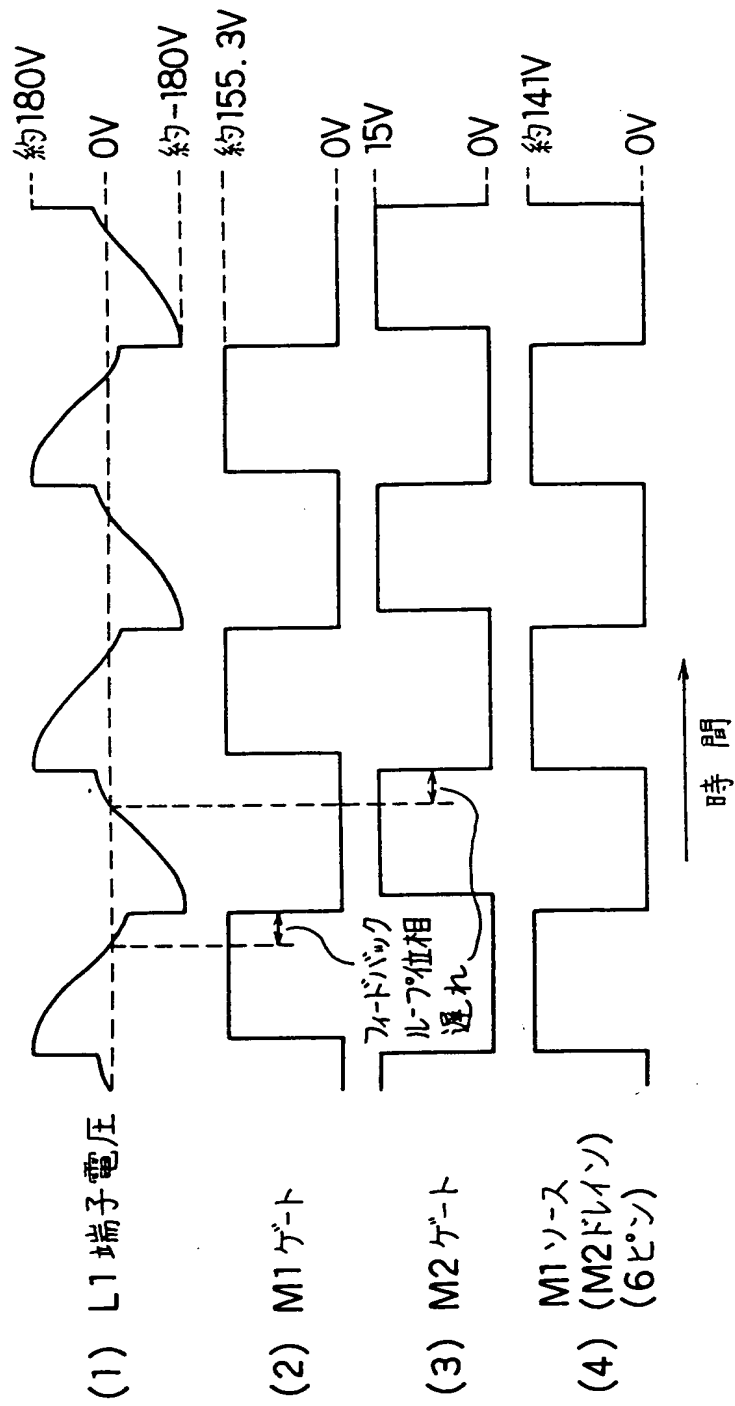


図 8

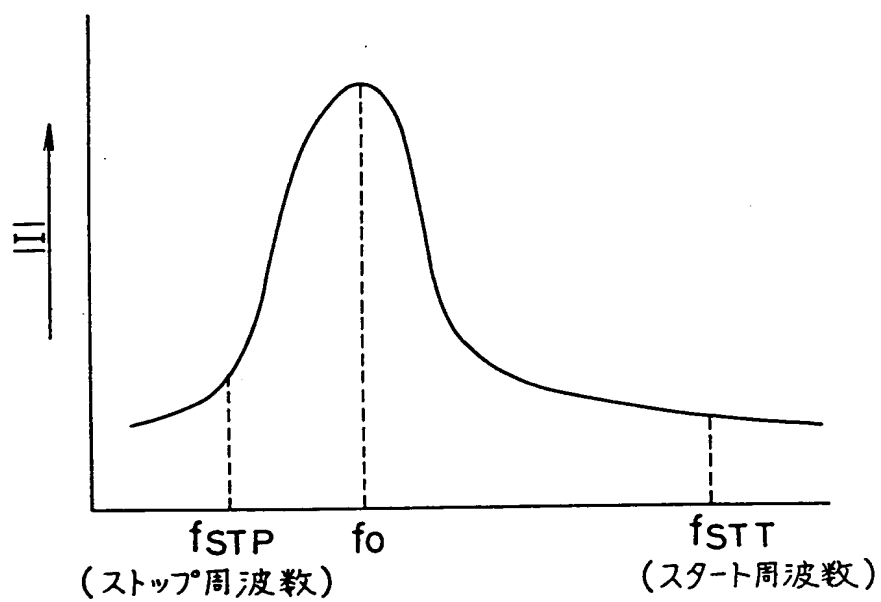
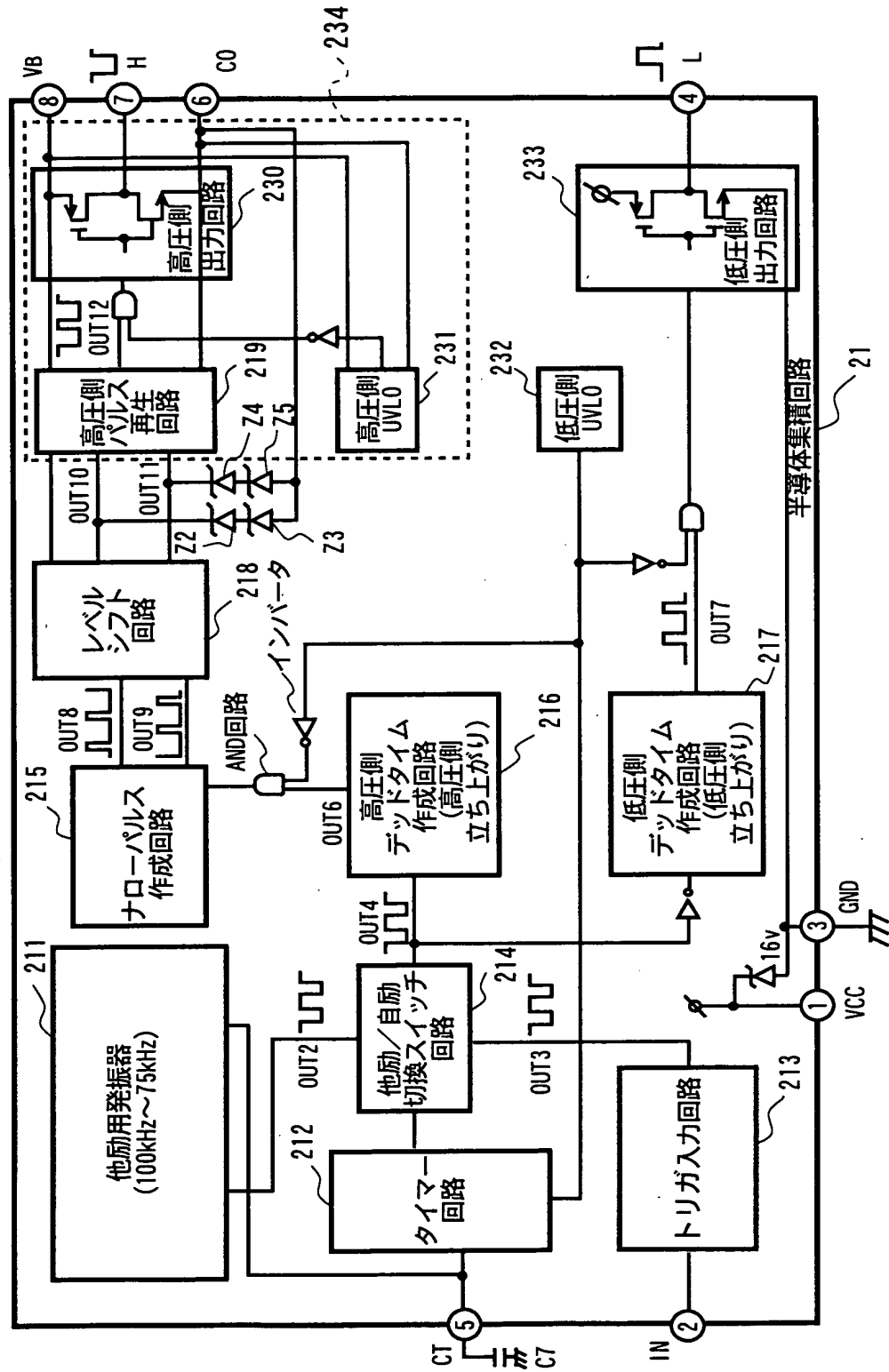


図9



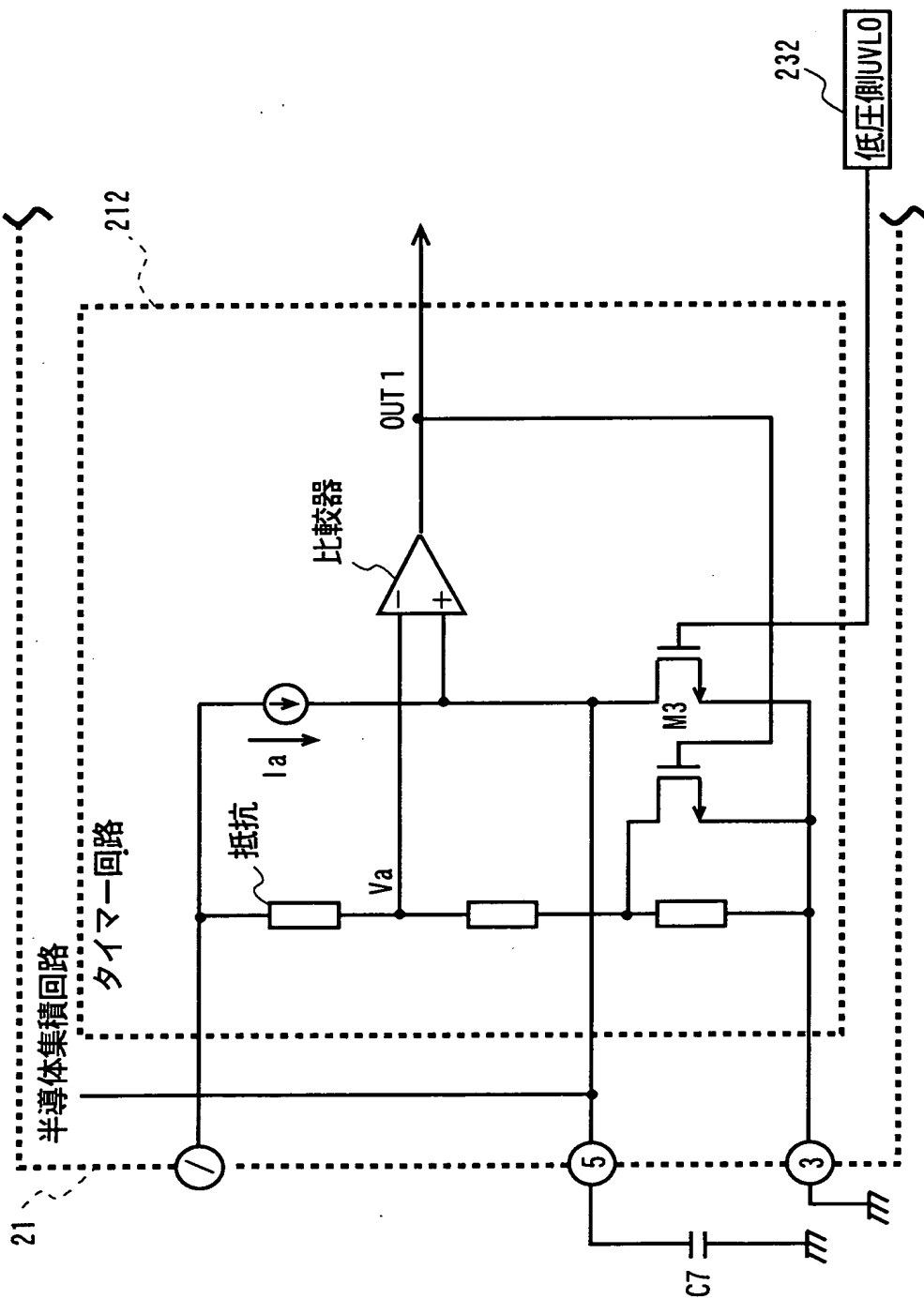


図10

図 11

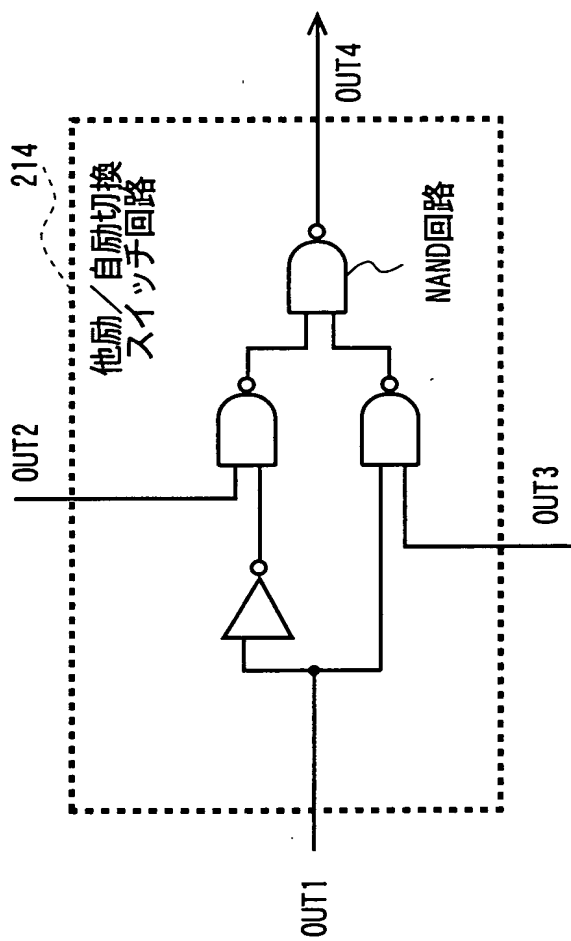


図12

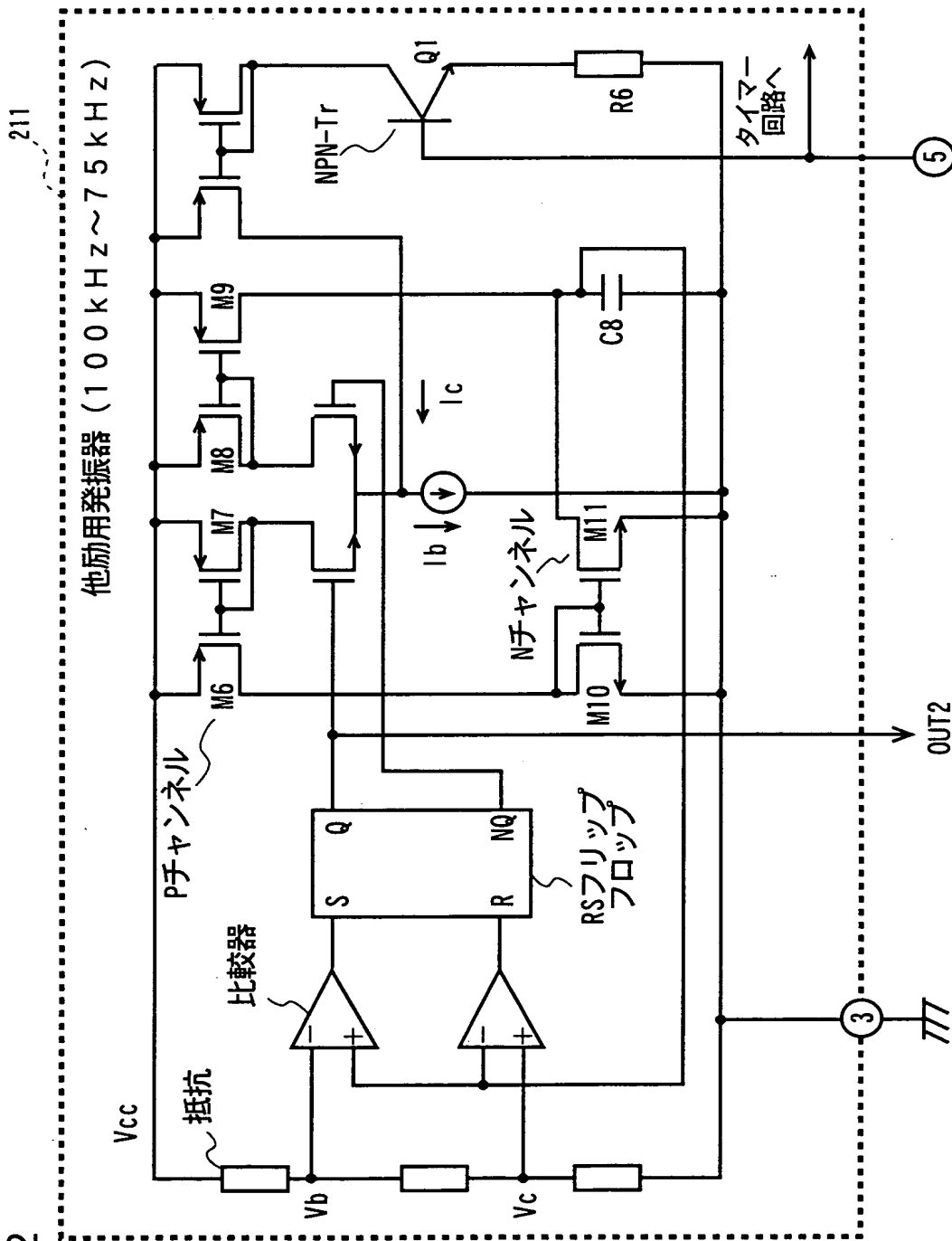


图 13

6602-SET-5160

(1) C8 端子間電圧

(2) OUT2

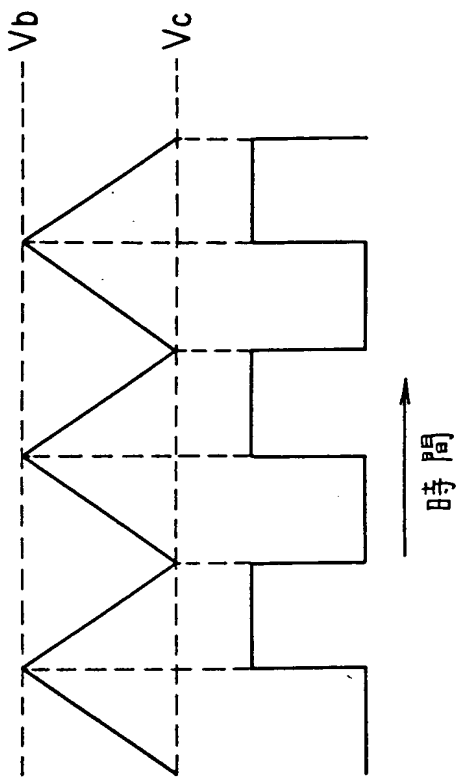


図14

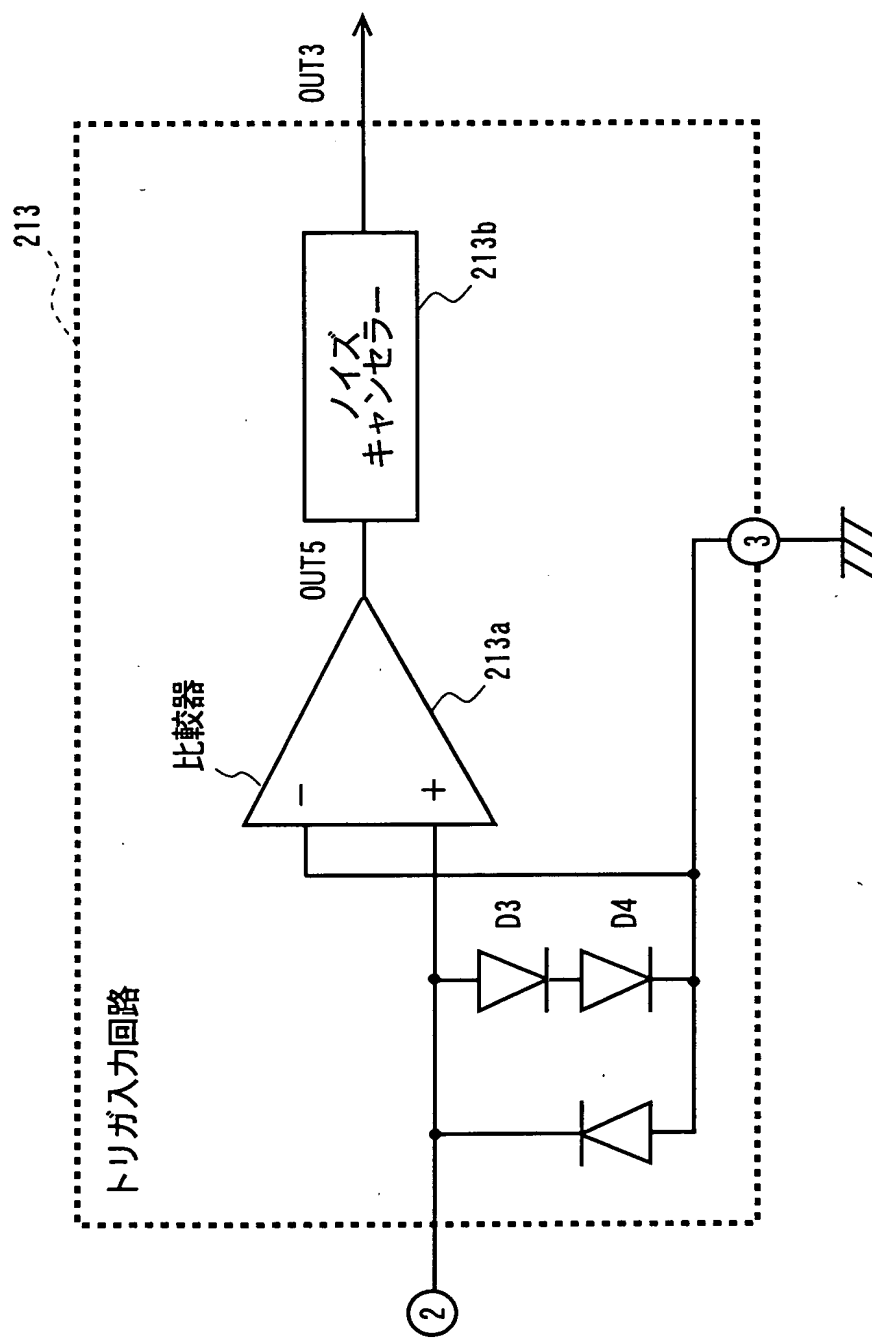


図15

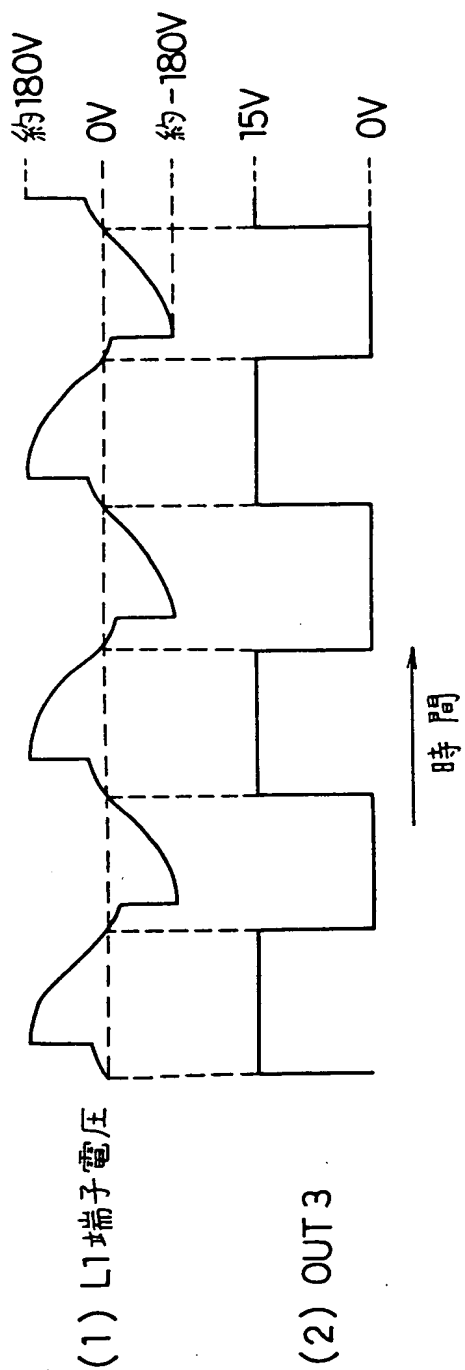
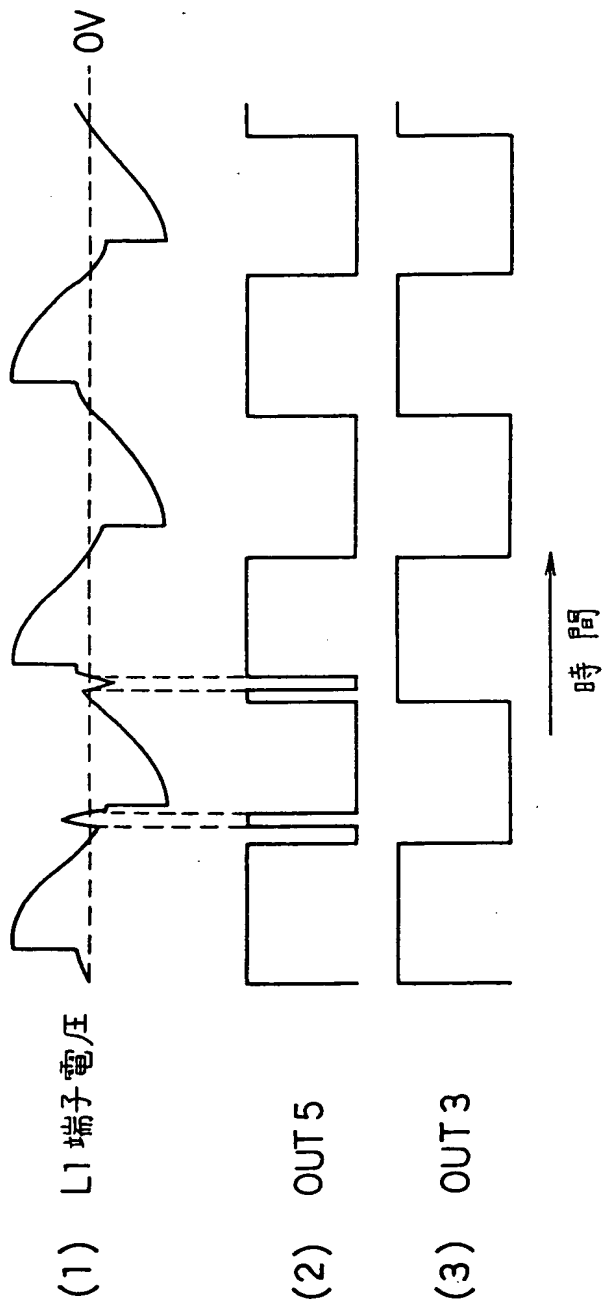


图16



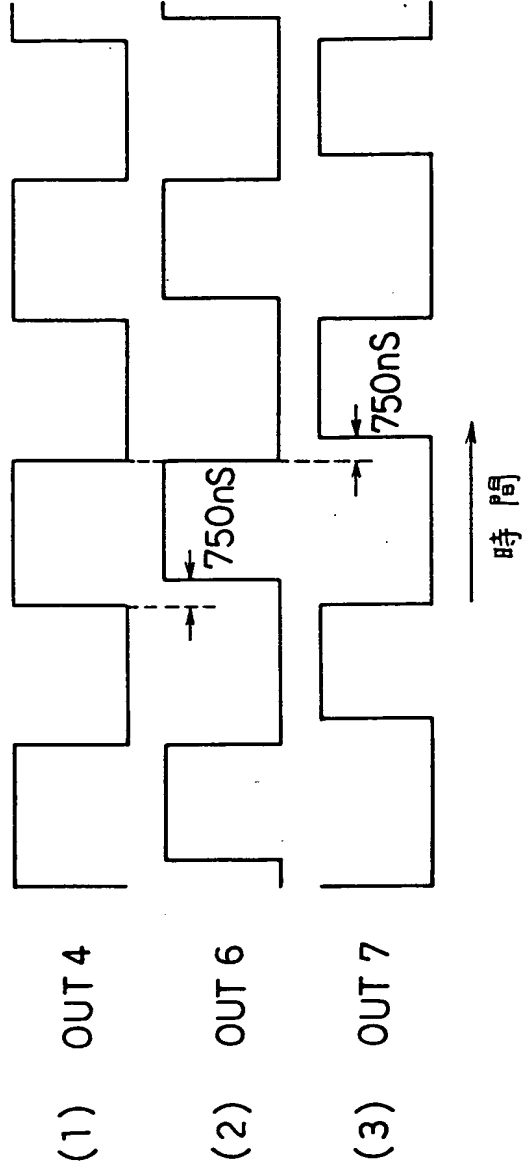


図18

66602T SET-45450

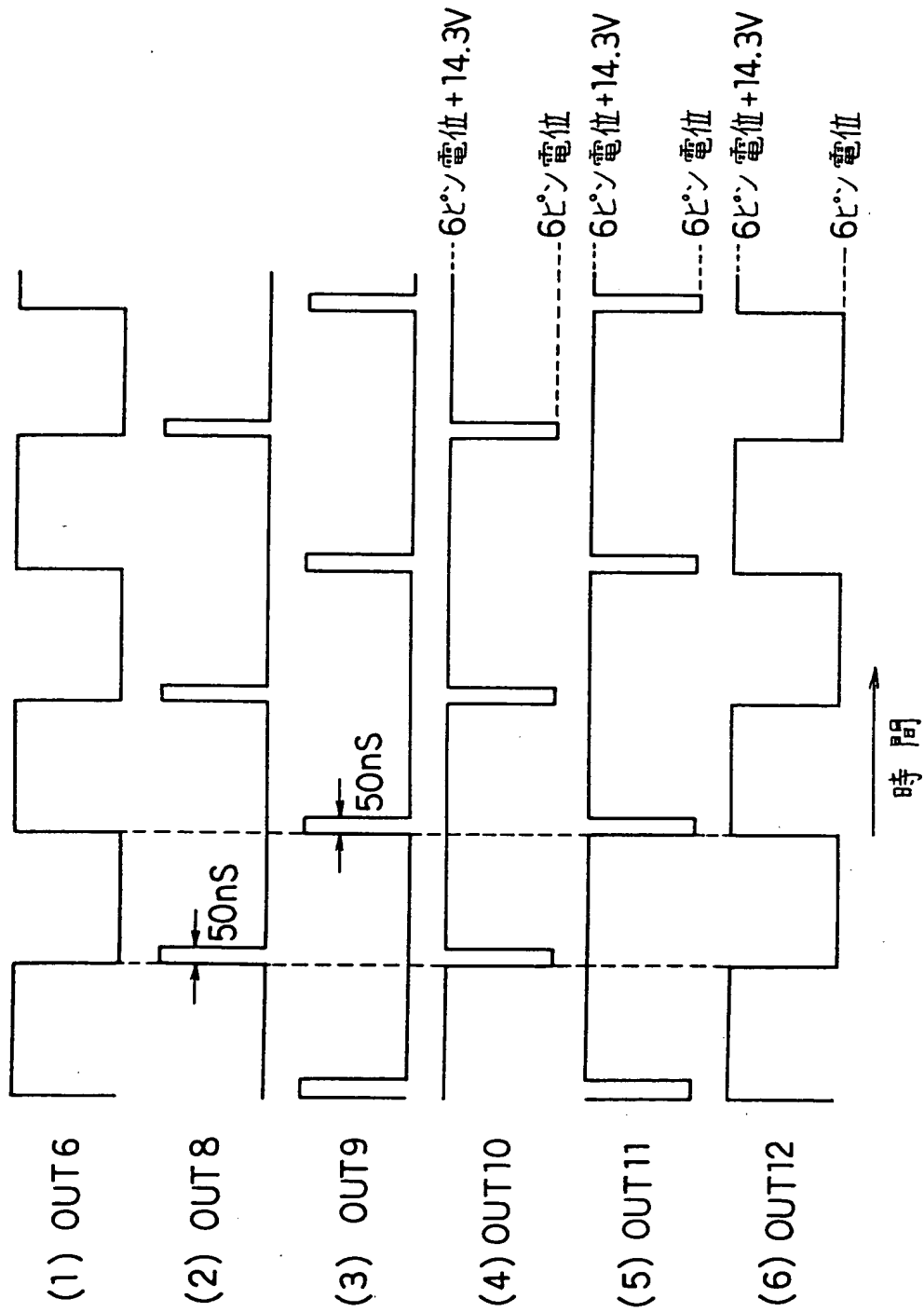


図19

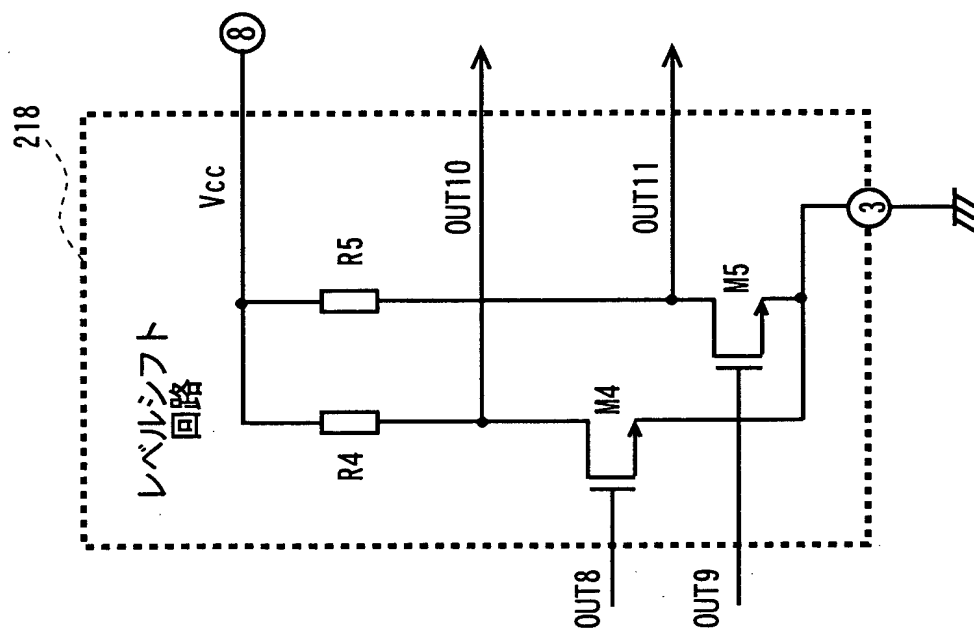
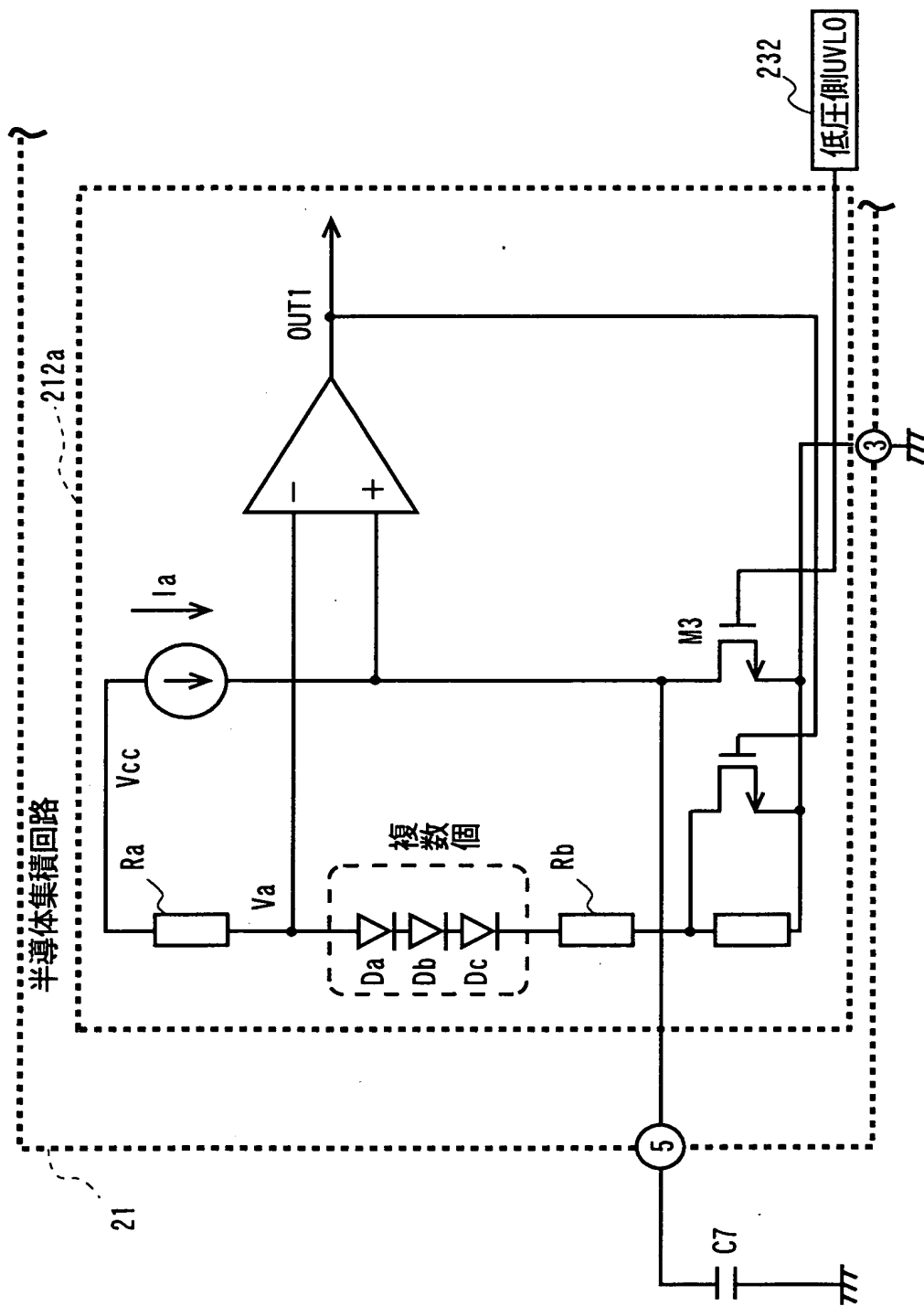


図 20



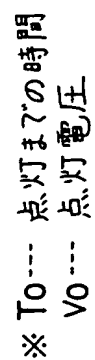


図 22

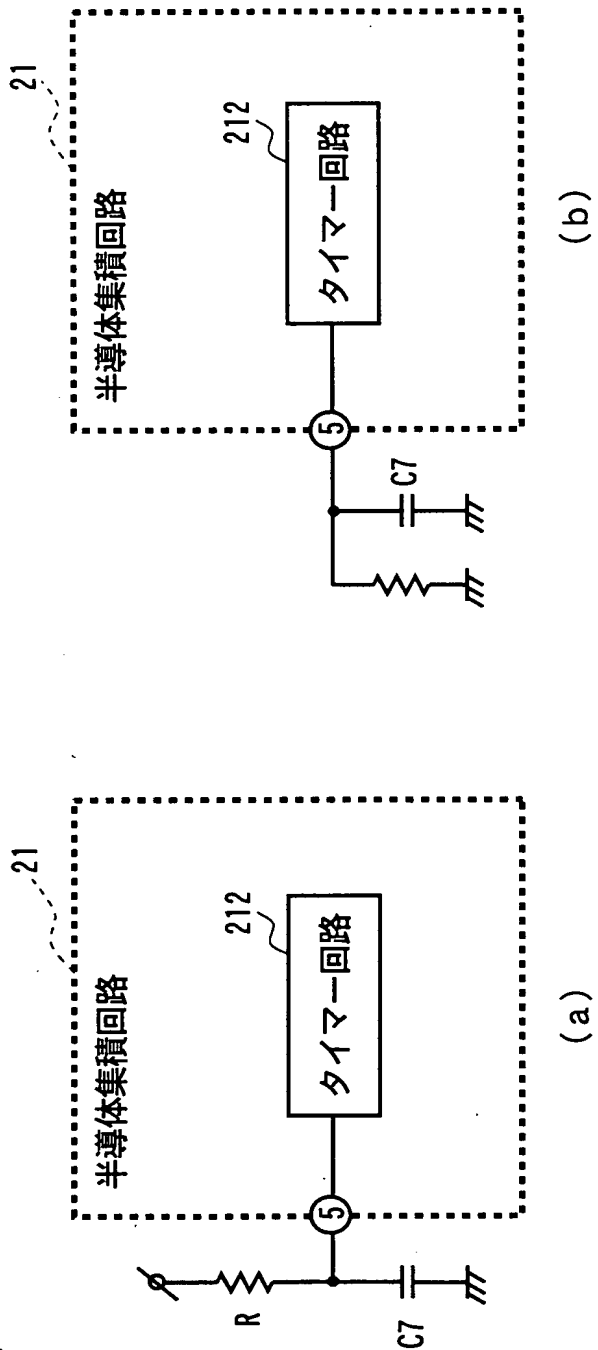


図 23

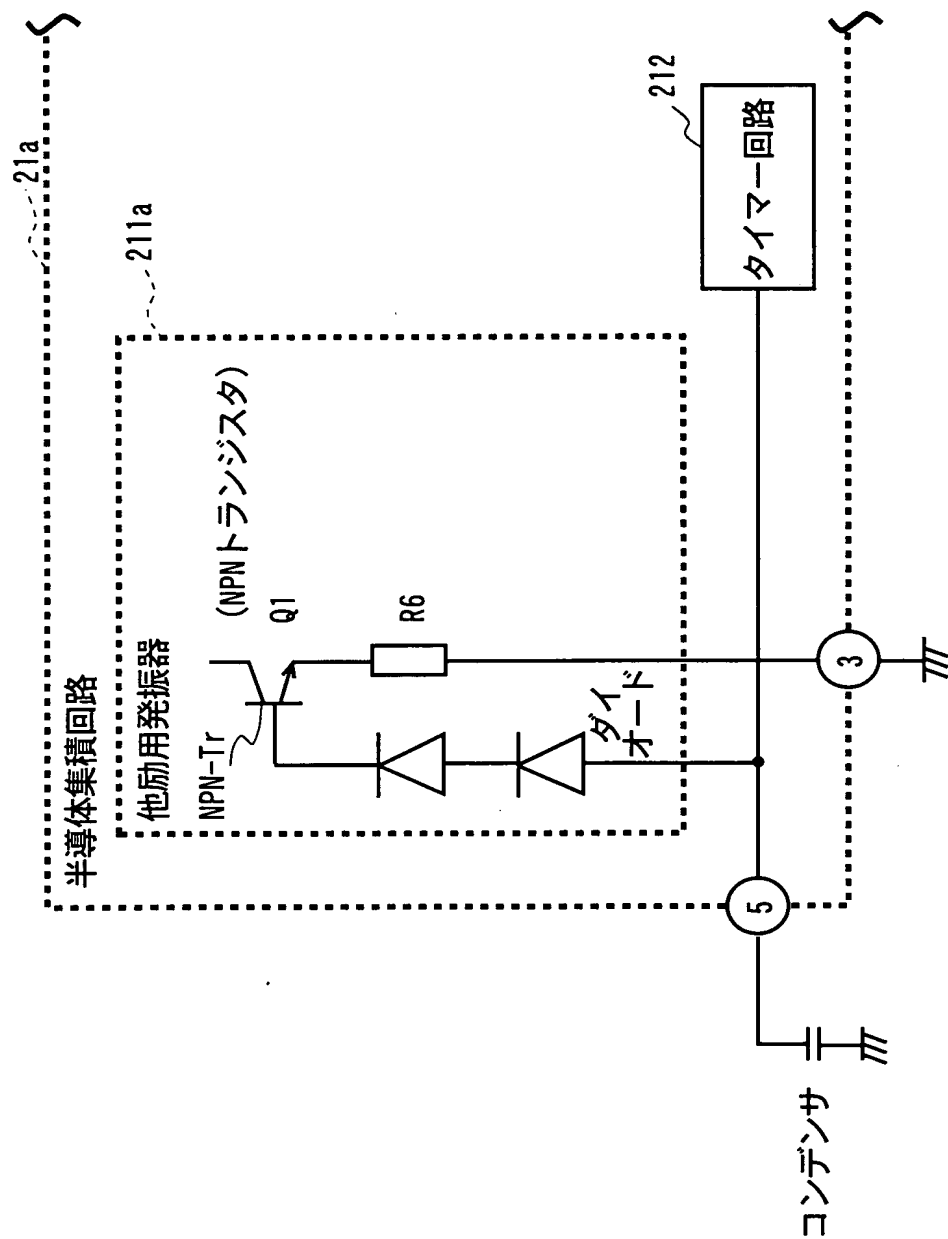
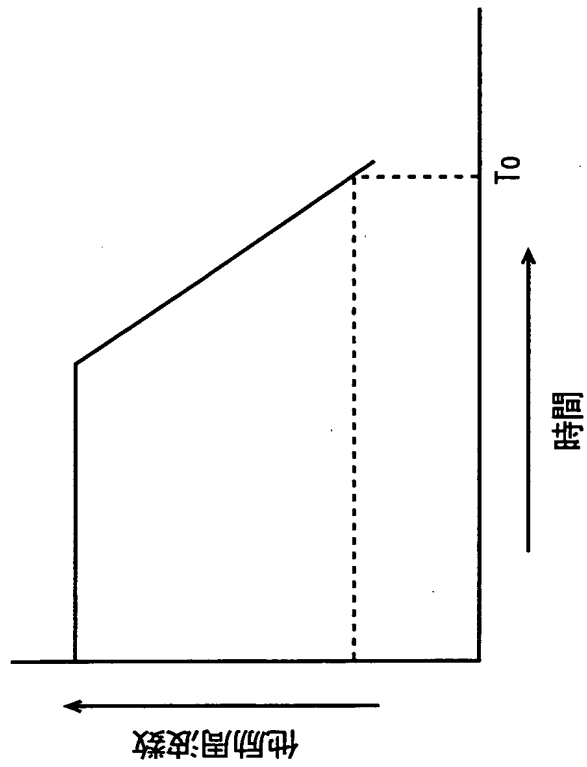


図 2 4



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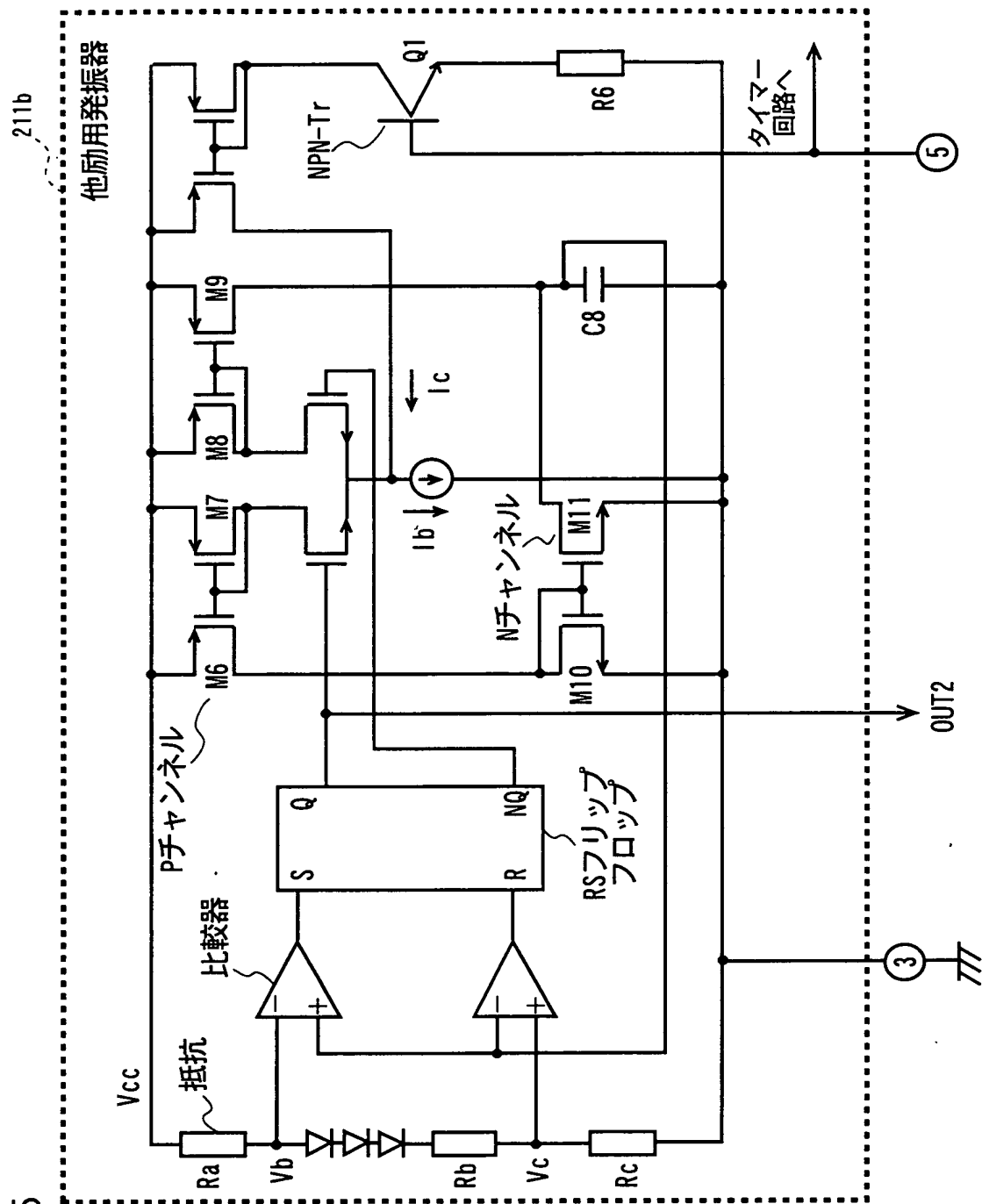


図 2 6

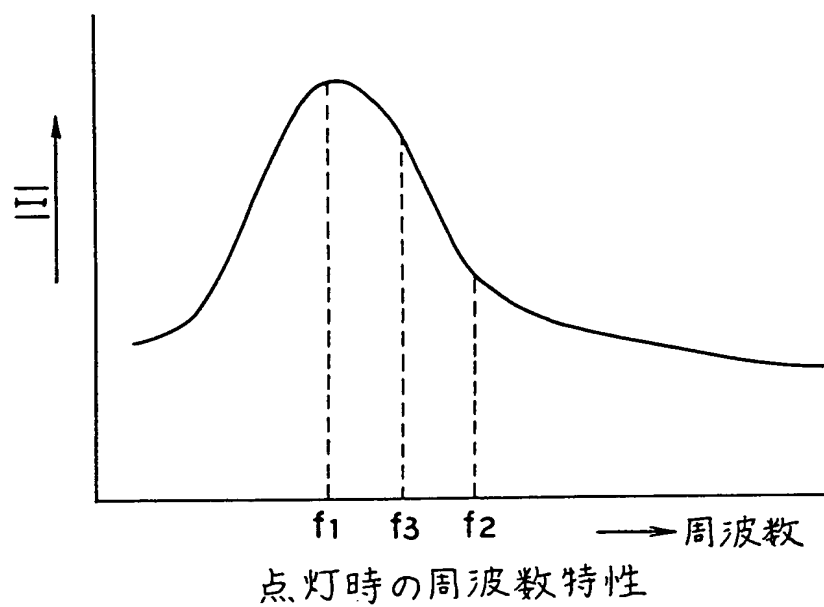


図 2 7

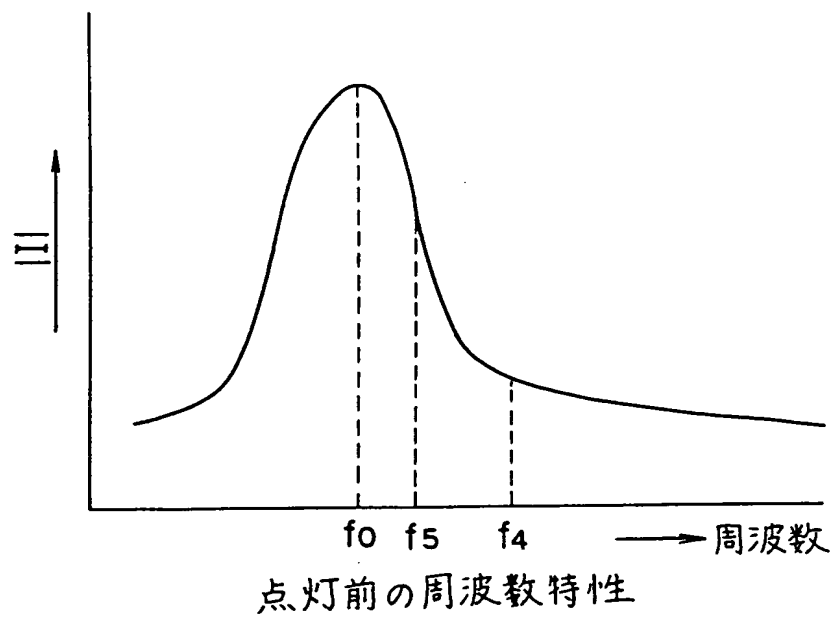


図 28

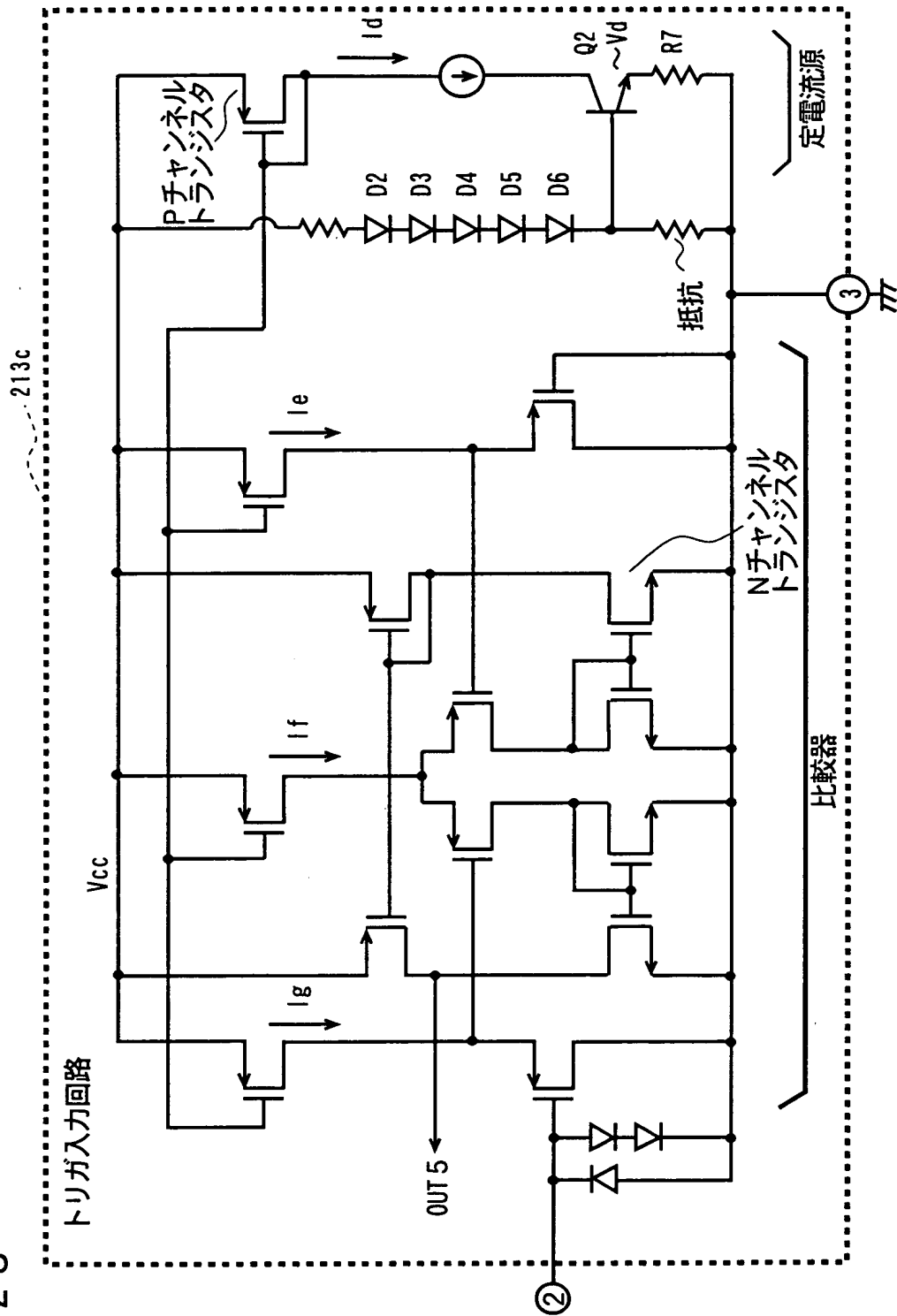


图 29

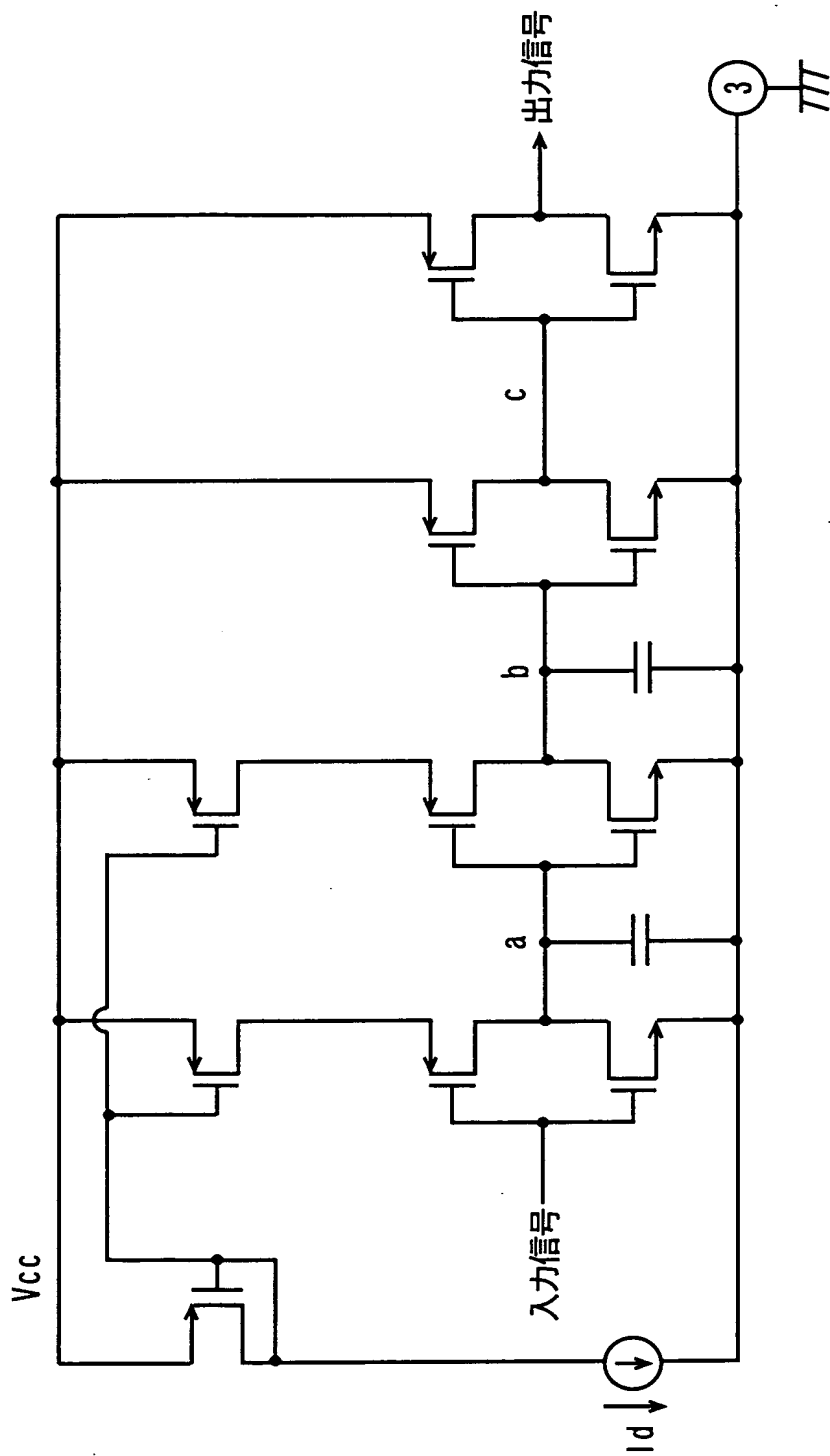


图 30

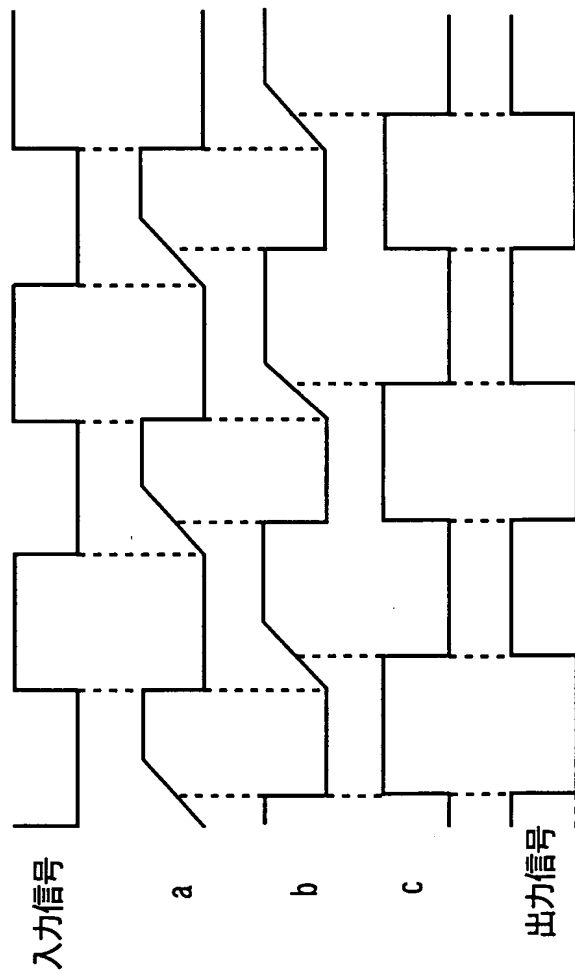


図 3 1

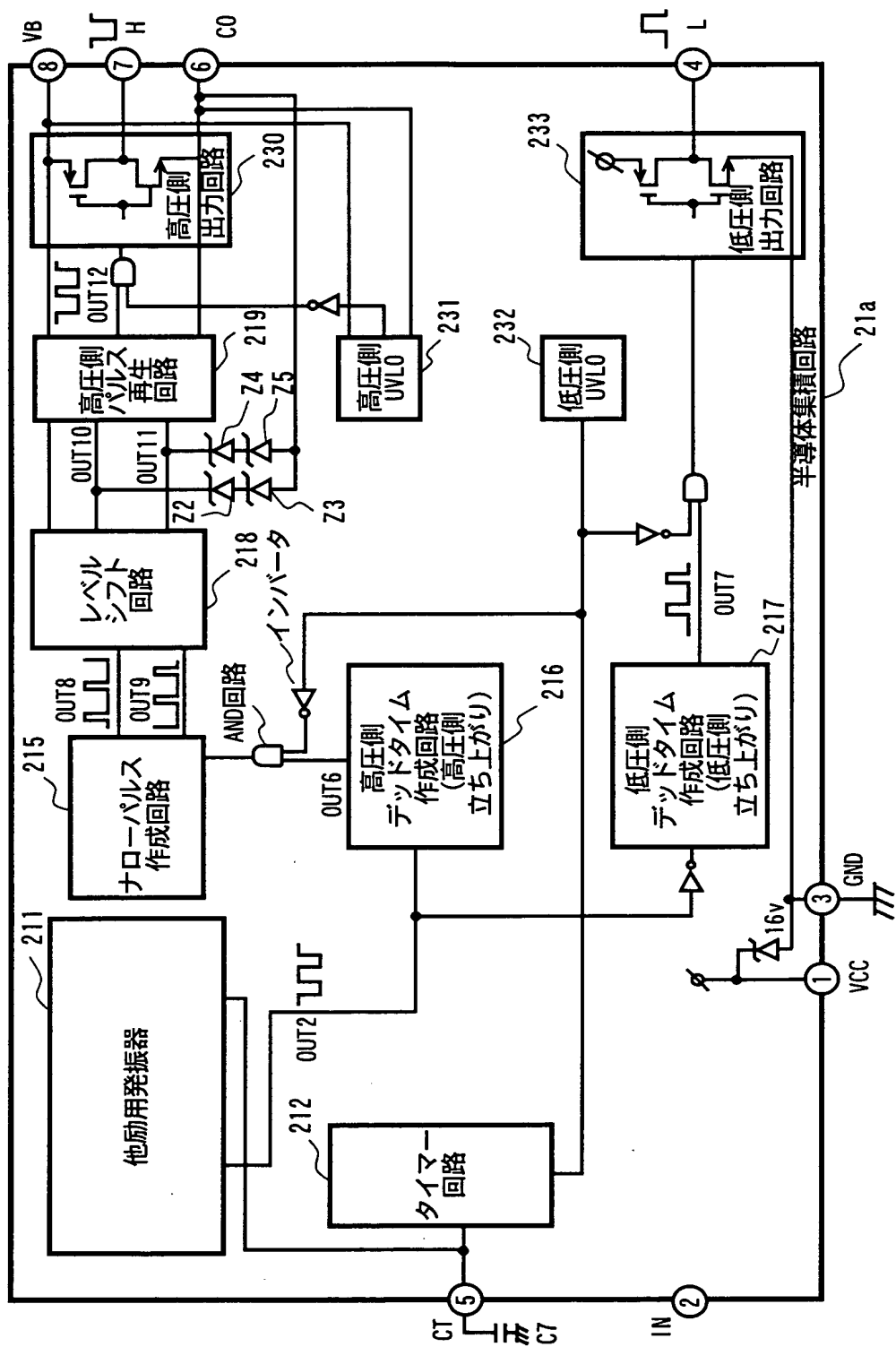
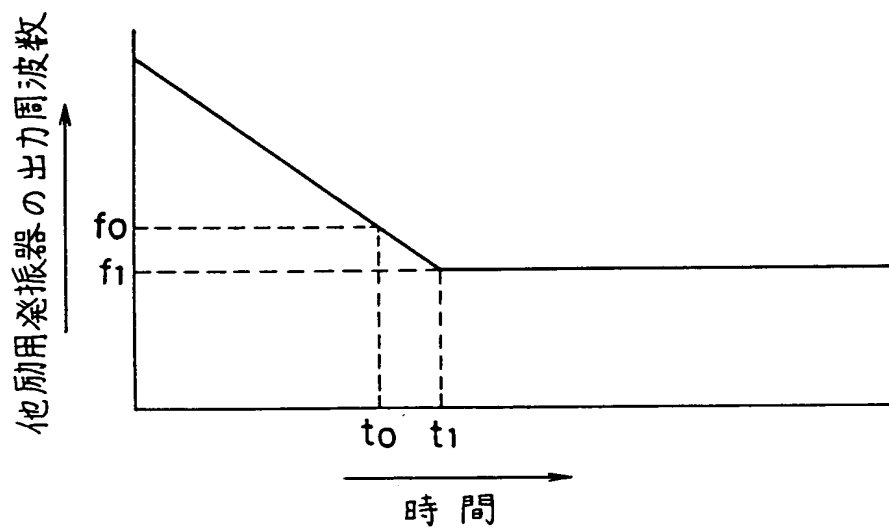


図 3 2



[illegible]

半導體集積回路

図 3 4

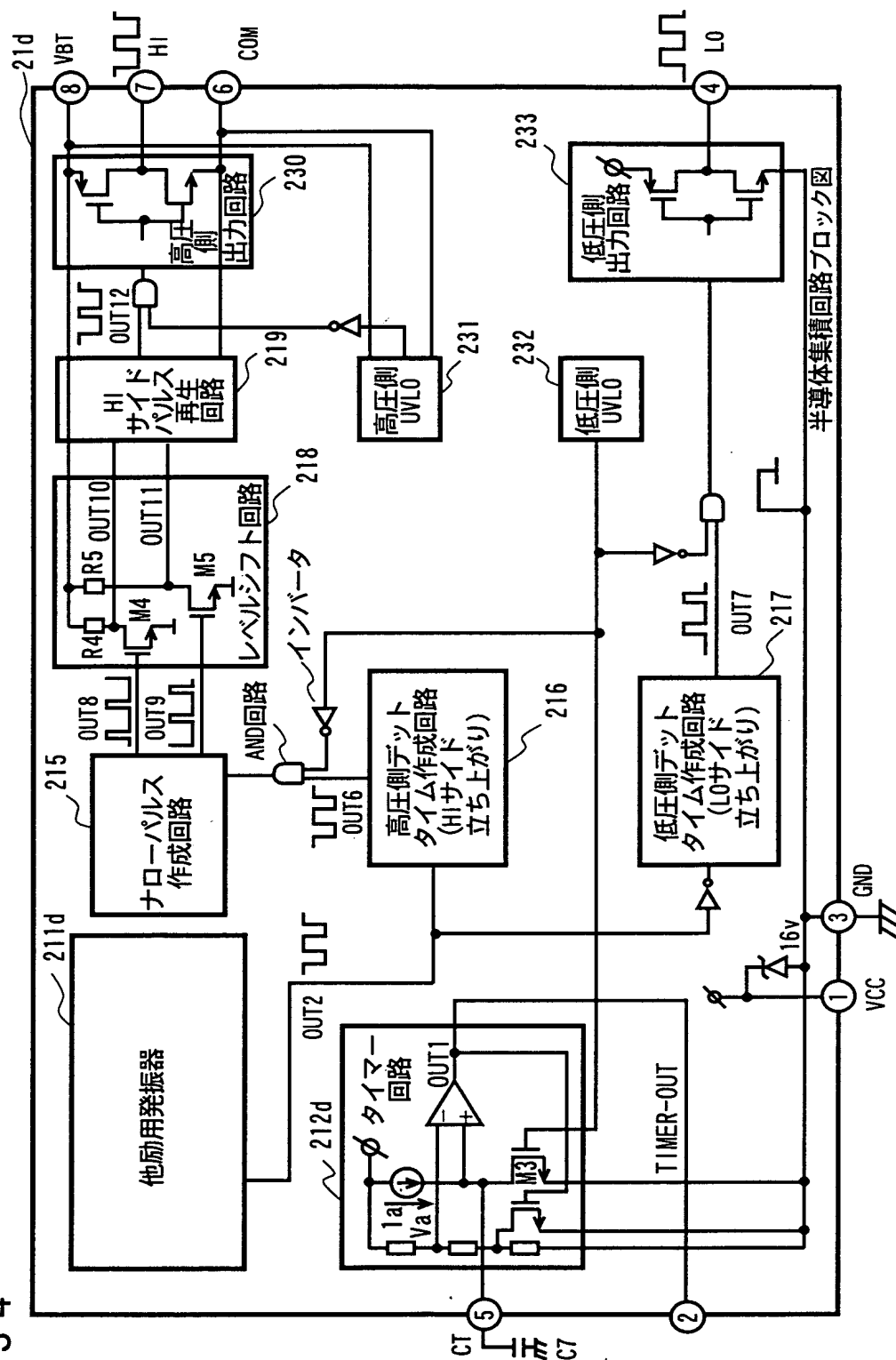


図 3 5

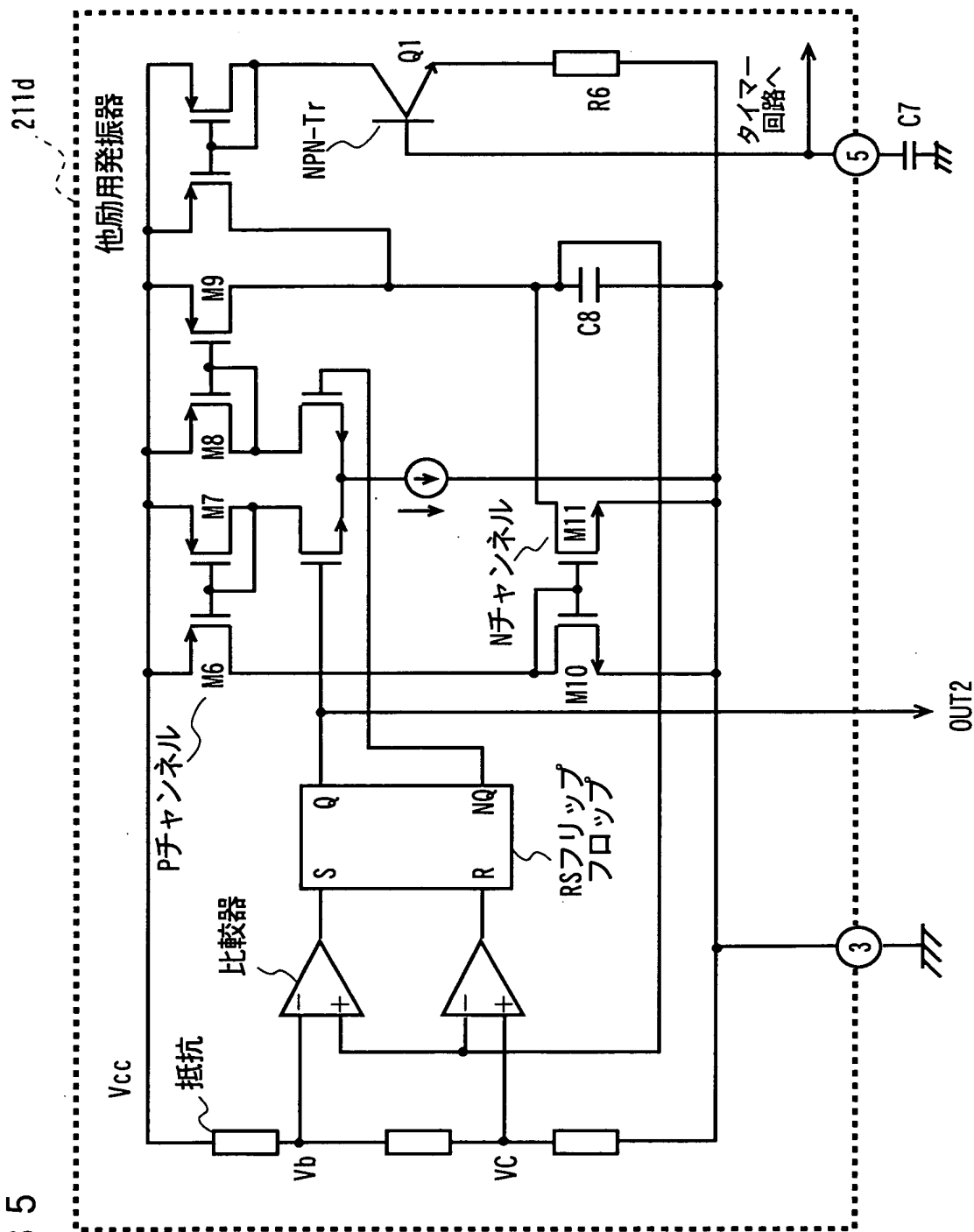


図 3 6

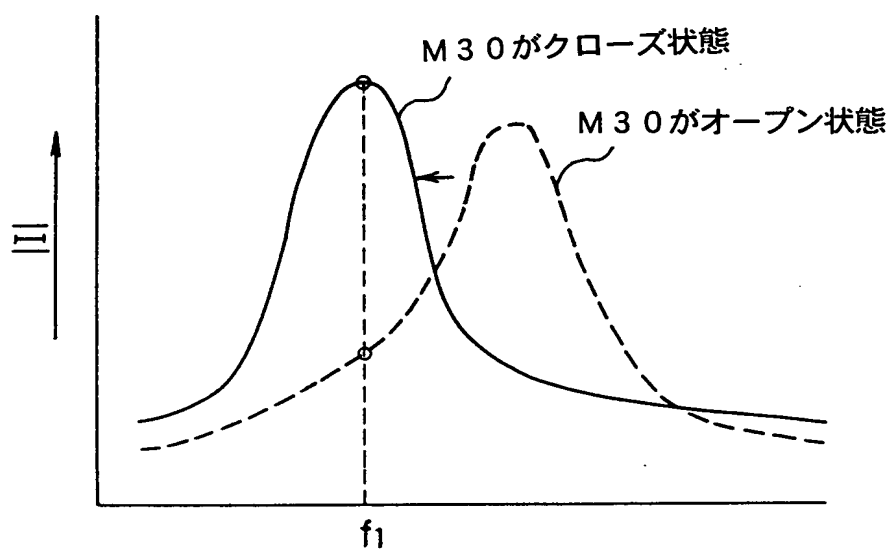


図 37

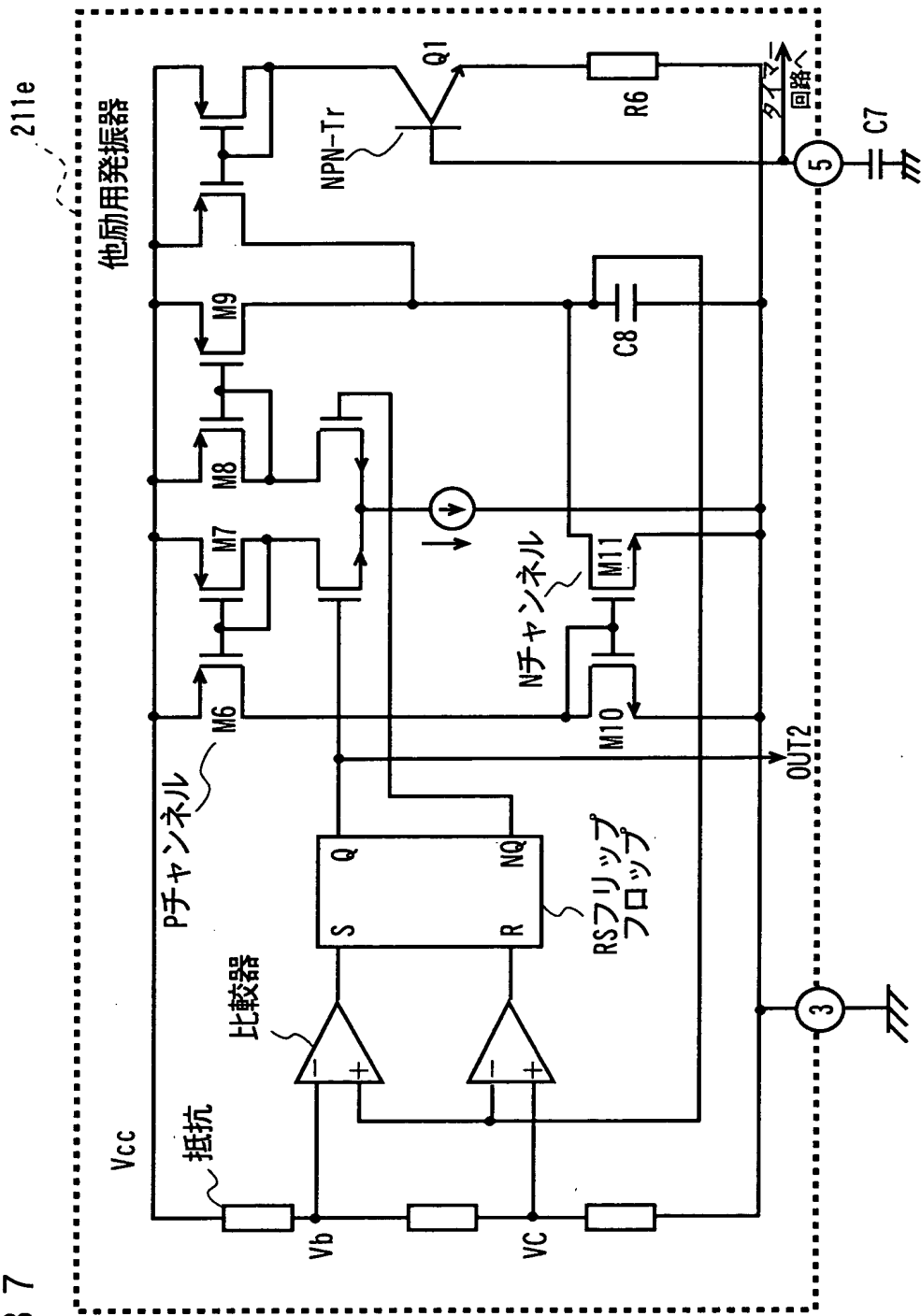


図 38

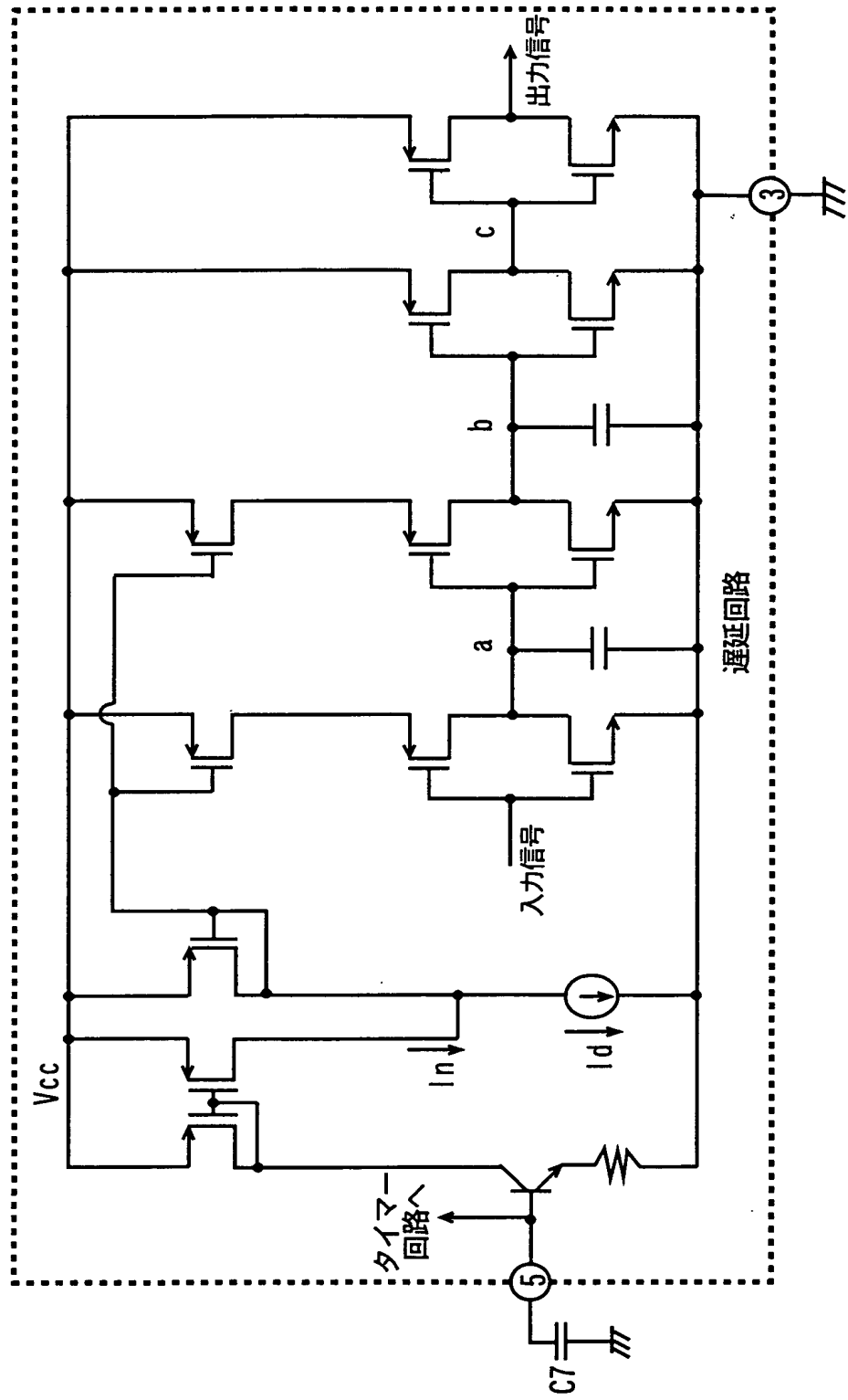
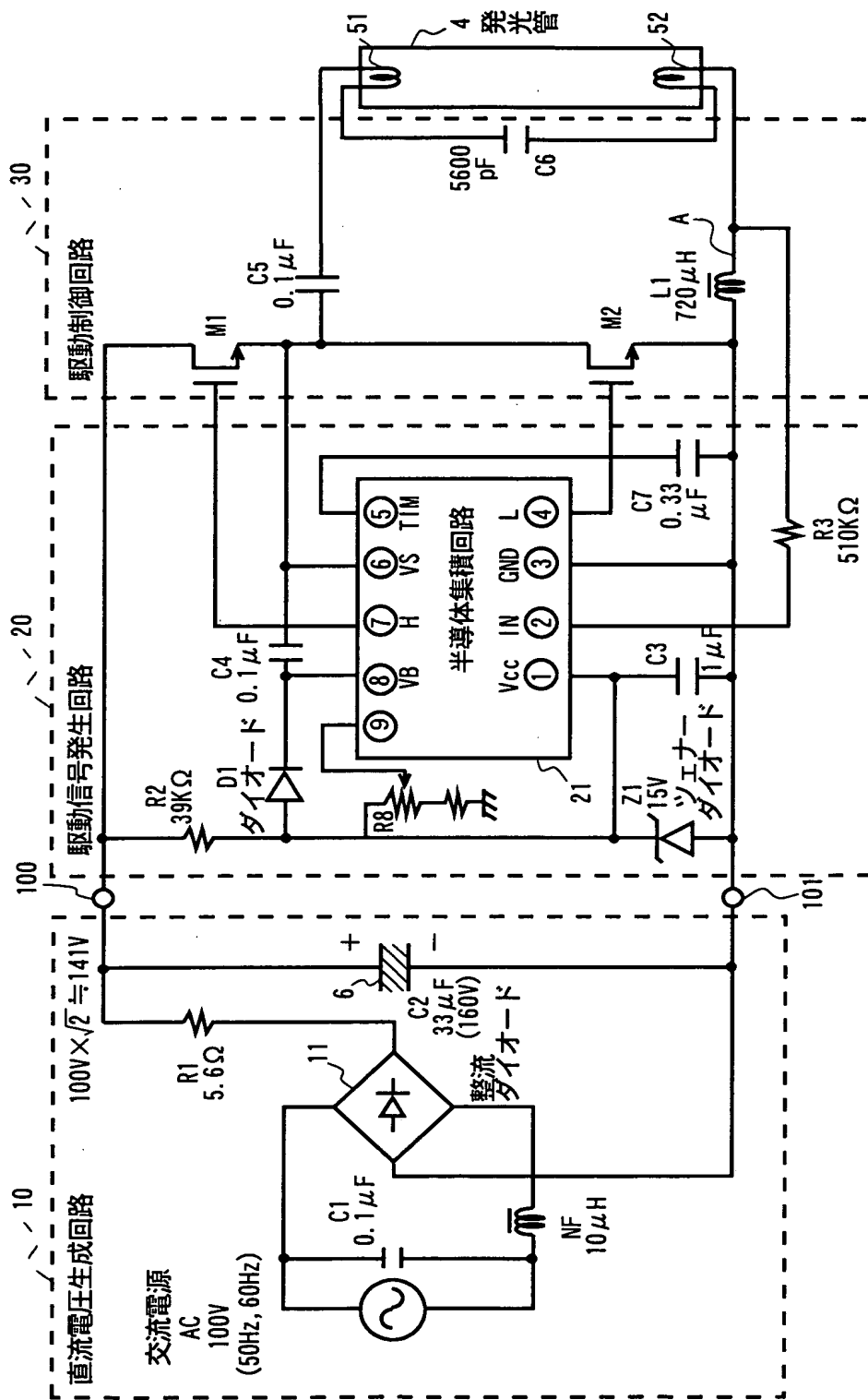


図 39



40

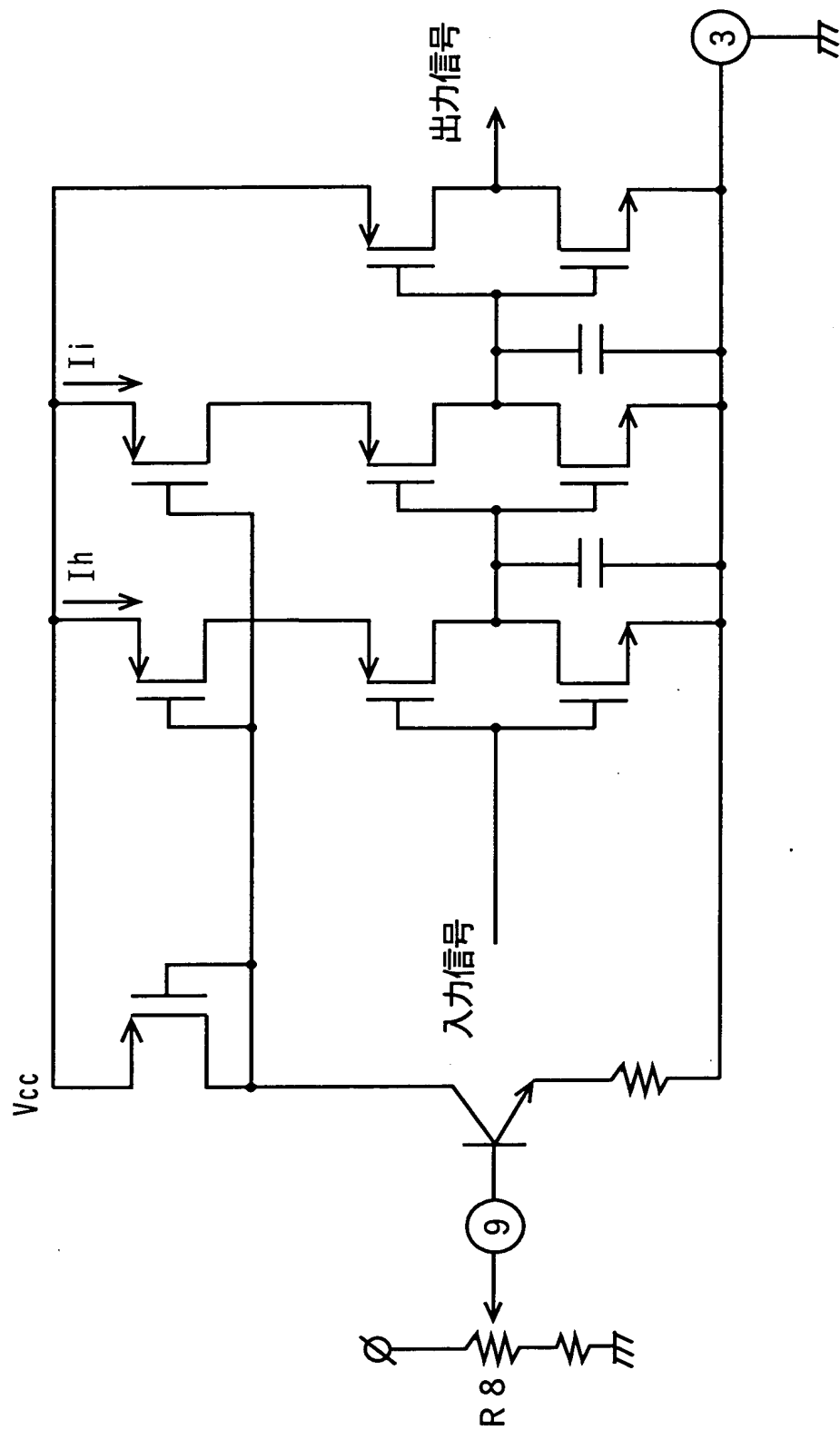


図 4 1

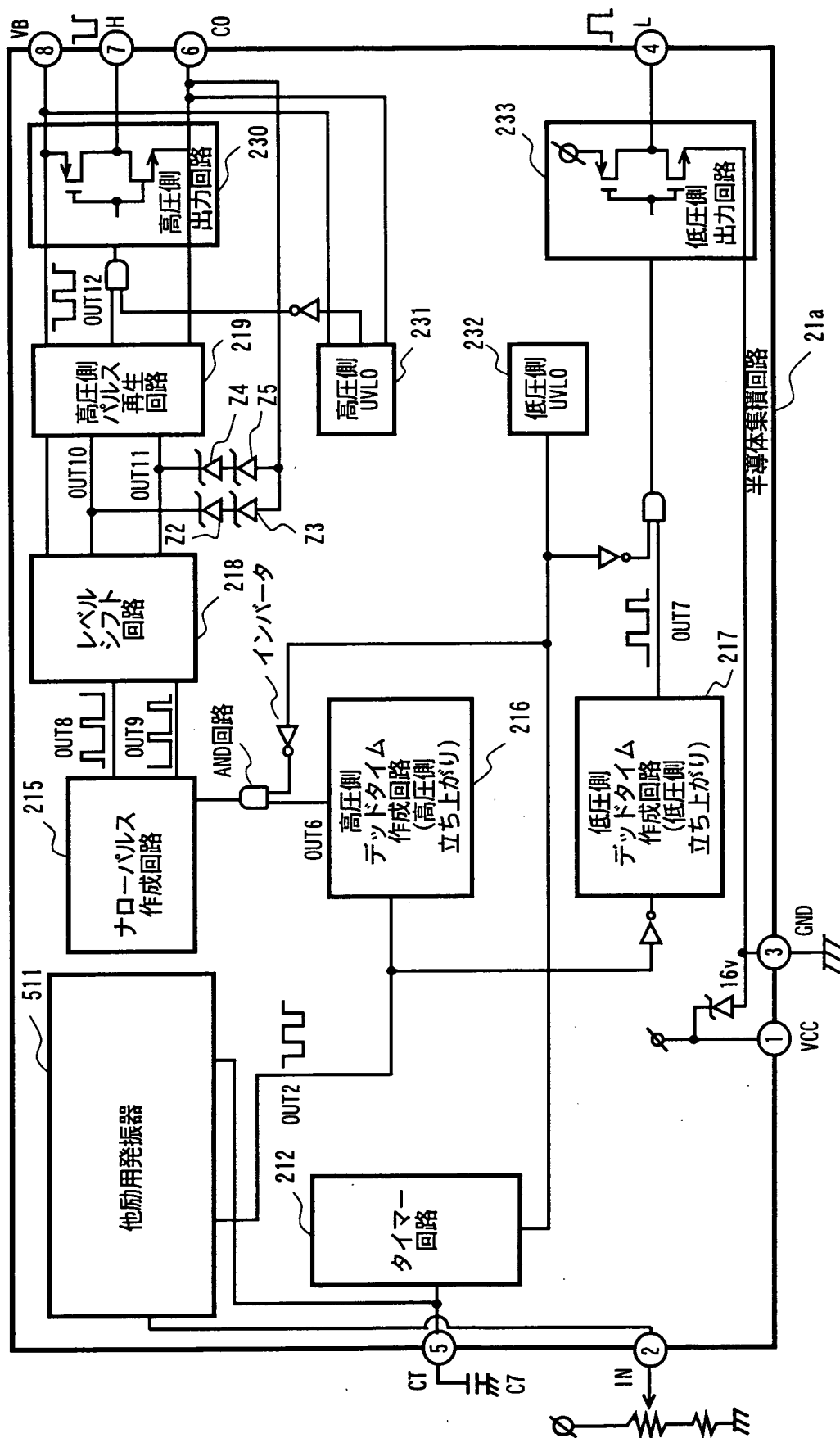


図 4 2

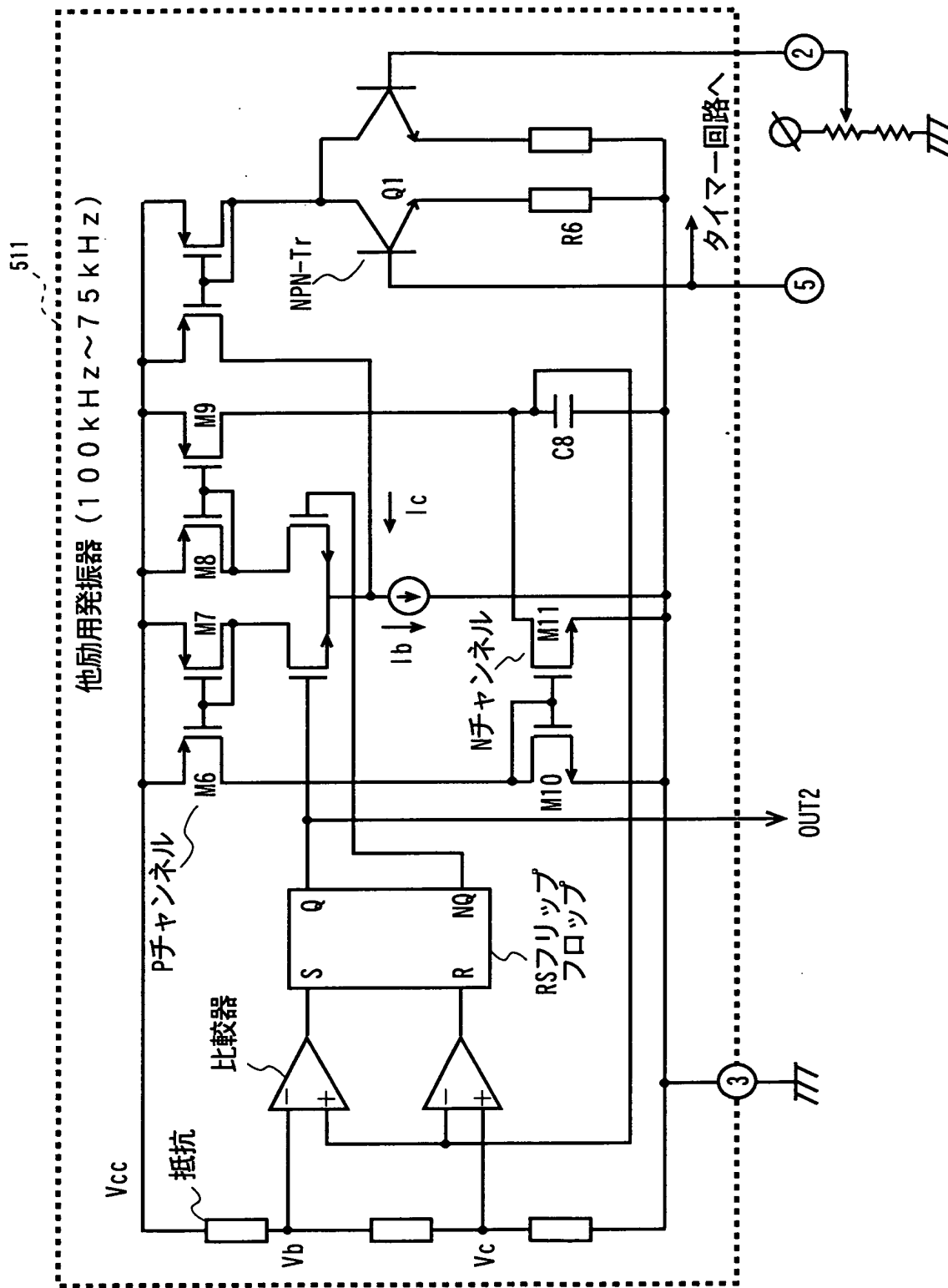


図 4 3

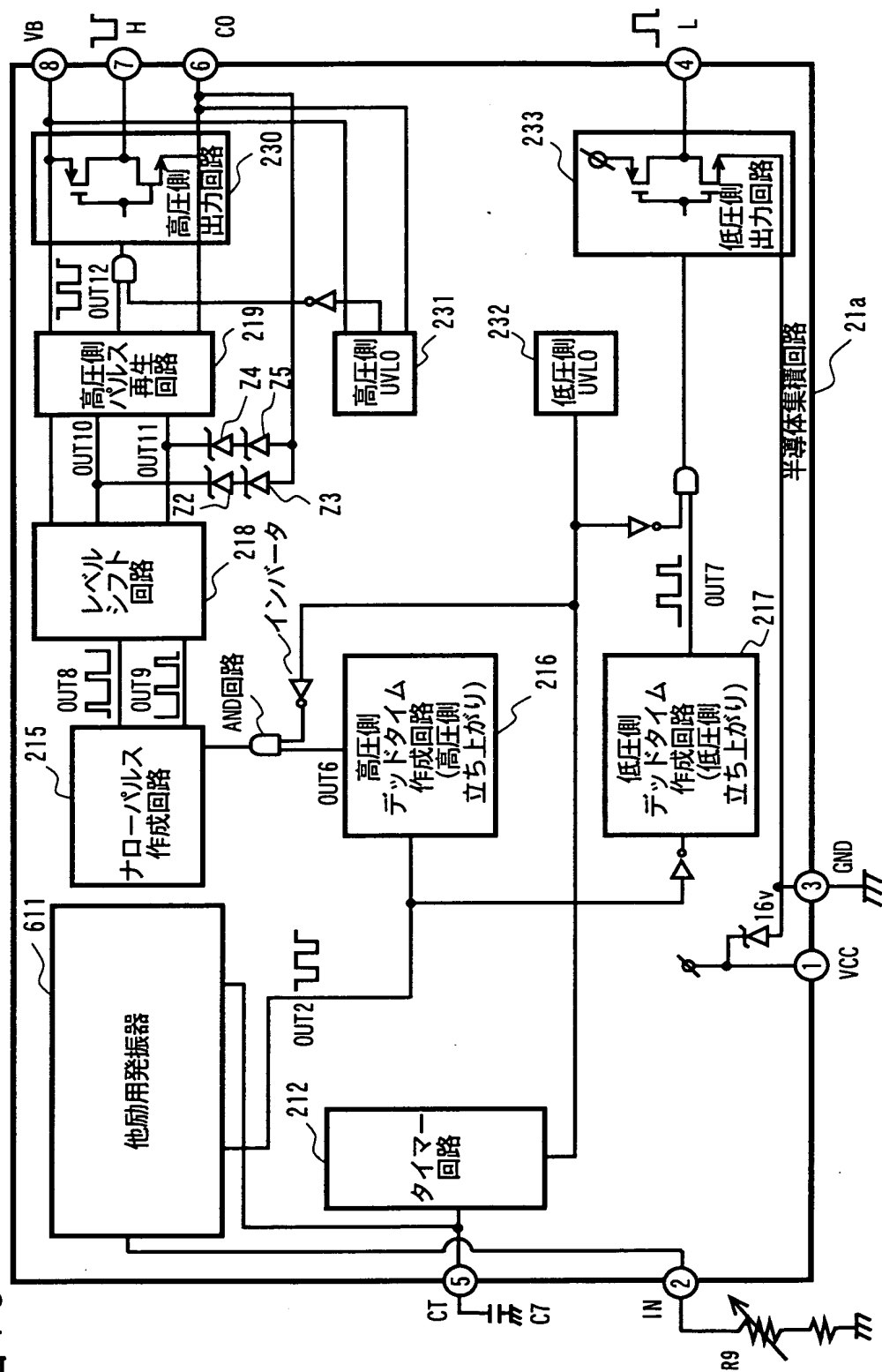


図 4 4

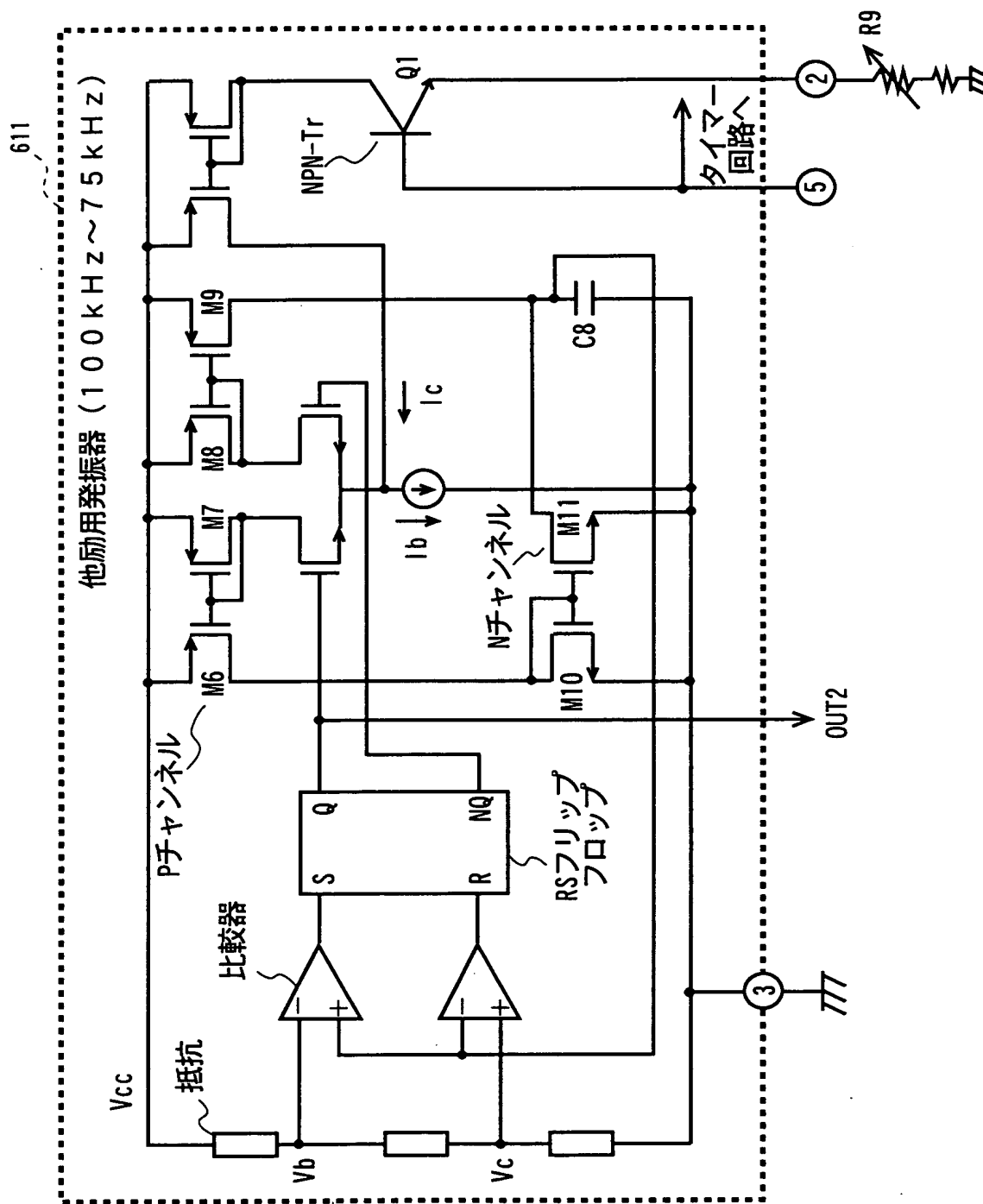
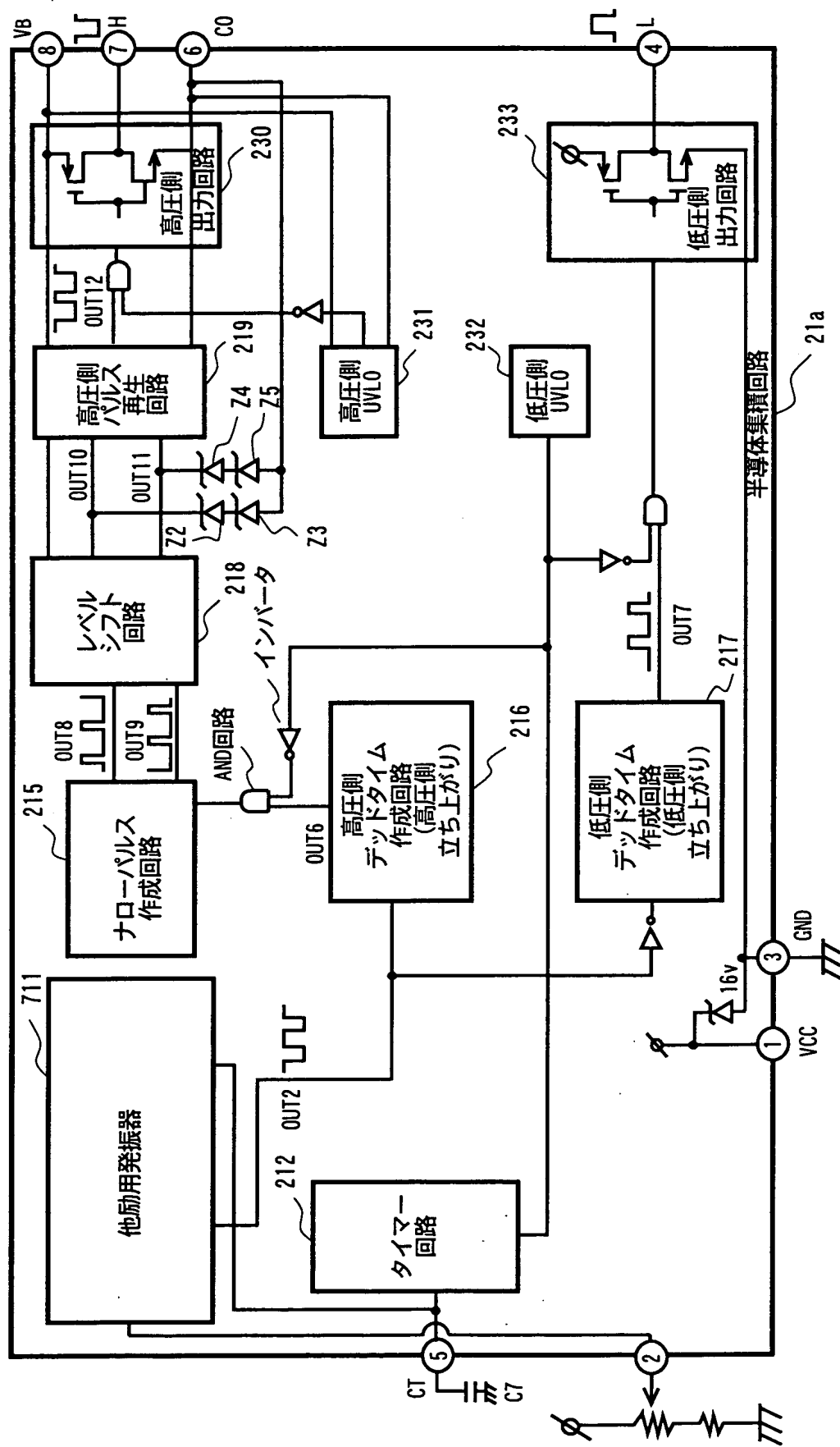


図 4 5



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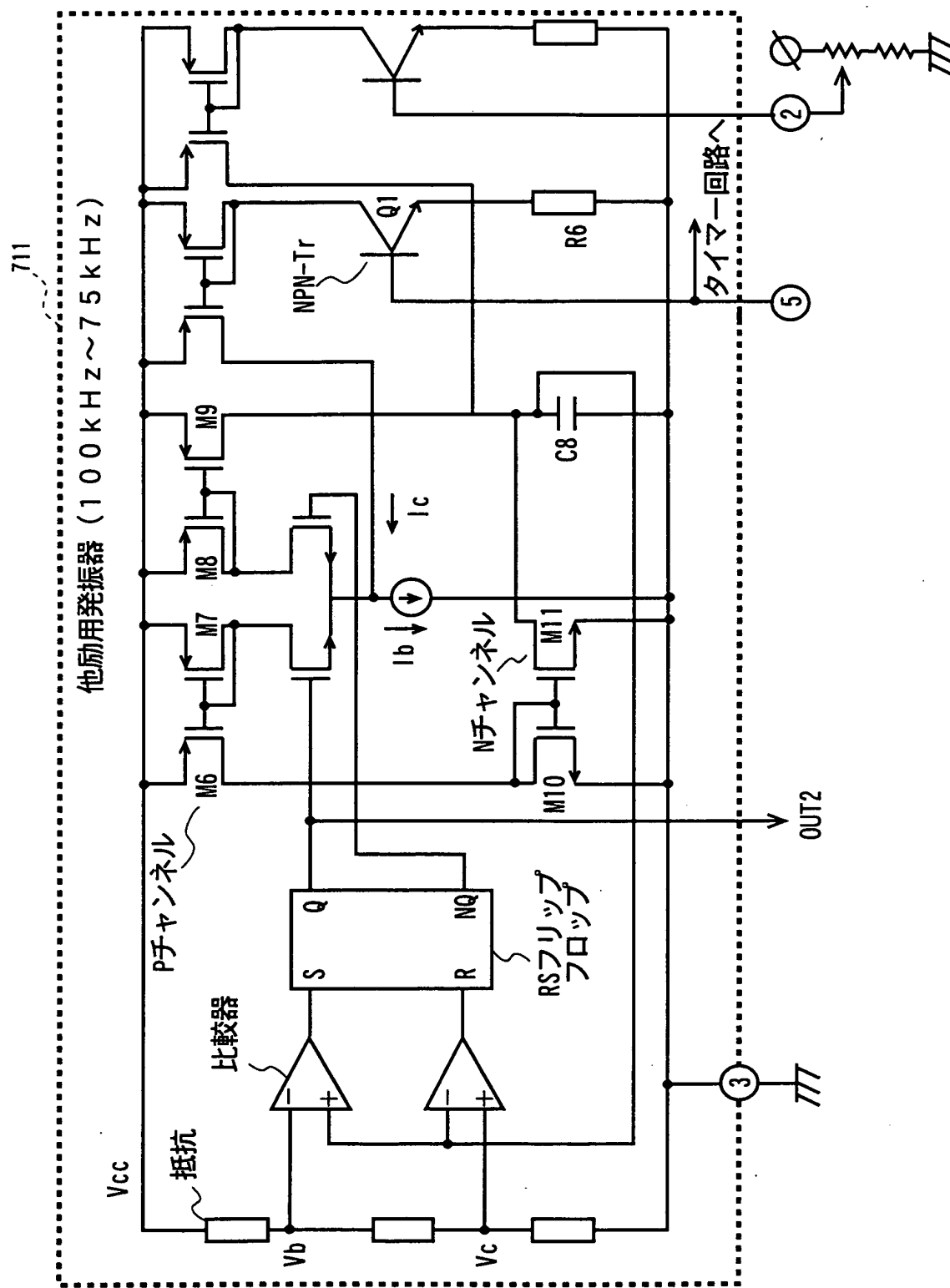


図 47

